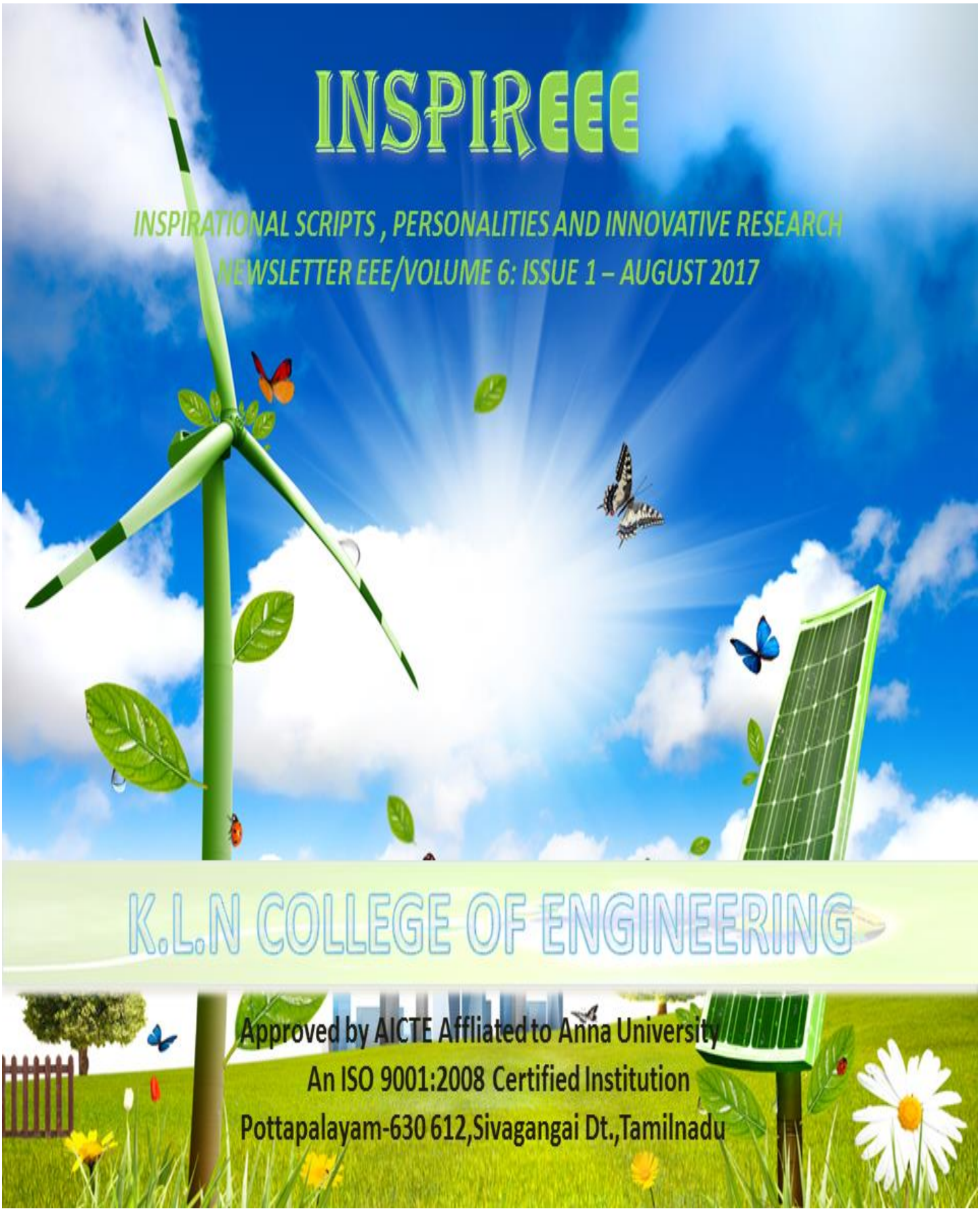

INSPIREEE
INSPIRATIONAL SCRIPTS , PERSONALITIES AND INNOVATIVE RESEARCH
NEWSLETTER EEE/VOLUME 6: ISSUE 1 – AUGUST 2017
K.L.N COLLEGE OF ENGINEERING
Approved by AICTE Affliated to Anna University
An ISO 9001:2008 Certified Institution
Pottapalayam-630 612,Sivagangai Dt.,Tamilnadu

INSPIREEE

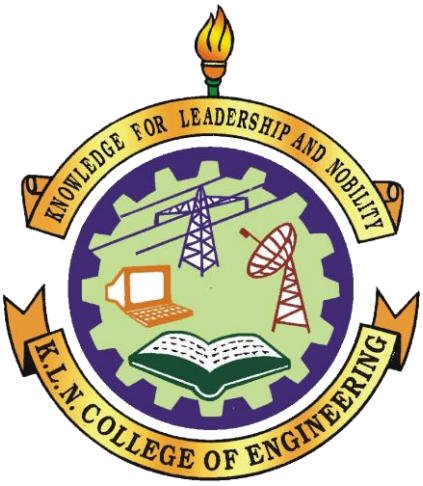
INspirational **S**cripts, **P**ersonalities and **I**nnovative **R**esearch of **EEE**

VISION

To become a high standard of excellence in Education, Training and Research in the field of Electrical and Electronics Engineering and allied applications

MISSION

To Produce excellent, innovative and Nationalistic Engineers with Ethical values and to advance in the field of Electrical and Electronics Engineering and Allied Areas



K.L.N. College of Engineering

Pottapalayam – 630 612, Sivagangai District, Tamil Nadu, India

INDEX

MESSAGE FROM HEAD OF THE DEPARTMENT

Greetings,

In this issue, various technical competitions, organized by companies like TCS, NI, TI and ELECTRAMA etc., briefed. This gives some insight into the level of technical competency to be attained, how to prepare to face such competition explained, in a systematic manner. Participation in such technical competitions will not only improve the technical competency, give an opportunity to become a part of such associations, by getting internship / inplant training. These opportunities are knocking our doors, and it is upto as to develop an attitude to make use of it.

Final year students who have attended Campus recruitment, shared their experience in this issue. How to prepare for Campus recruitment, depends on company. The requirements of companies like Aricent, Infoview, etc. nicely explained by the students.

This issue also brings out students participation in various events like workshop, conference, journal publication, placed student details, inplant training, etc. This is to motivate the second and third year students, to regularly participate in such academic activities, to update themselves to facilitate good progress.

Best wishes

Dr. S.M. KANNAN

Head of the Department - EEE

EDITORIAL CREW

EDITOR IN-CHIEF:

Dr. S.M. KANNAN [Professor & Head]

EDITOR:

Dr. S.P. RAJARAM [Assistant Professor 2]

STUDENT IN-CHARGE:

SURIYAKUMAR V (142006 / IV Year C Section)

SATHISHKUMAR M (142052/ IV Year C Section)

ALIGNMENT & CORRECTION:

PONBALAMURUGAN M (142303 / IV Year B Section)

VIGNESHWARAN M (142005 / IV Year C Section)

POSTER CREATION:

THAMODHARAN P (152904 / IV Year C Section)

S.SWEETY ZIONY (152069 / III YEAR B SEC)

PURPOSE OF CODEVITA:

- Help TCS spot the bright students
- Provide students an opportunity to showcase their programming talent and earn peer recognition and honour
- Provide an opportunity to showcase offerings of TCS to the academic world.

STRUCTURE OF CODEVITA:

Round 1: This is the first round of the contest and will comprise of a series of real time problem statements.

Round 2: This is the final round of the contest. Top 300 or Top 5% of teams (whichever is higher) from the first round will move into this round.

Grand Finale: To be held in one of the TCSL offices. Top 3 teams will be declared as winners of the contest

Exciting Prizes to be won this Season:

- 1st Prize: USD 10,000 + Research Internship + Provisional offer
- 2nd Prize: USD 7,000 + Research Internship + Provisional offer
- 3rd Prize: USD 3,000 + Research Internship + Provisional offer

Eligibility Criteria:

Coders from institutes across India are eligible for this contest. Registrations are invited from current students in under graduate/post graduate disciplines from engineering with any specialization**.**

C.VIVEK (152914 / IV YEAR C SEC)

Importance of ELECRAMA:

- Largest congregation of power sector ecosystem in the geography.
- Widest Choice of product and technology across the spectrum on display – over 221Transformers, 321 Cables & Conductors, 202 Control & Switchgear, 113 Instruments & Instrumentation amongst others.
- Presence of global majors to small scale manufacturers – the entire value chain to strengthen supply chain, logistics & vendor capability assessment.
- Preview new and upcoming product & technology, new specs & standards.

BENEFITS OF ELECRAMA CONTEST:

Members benefit from IEEMA technical product-specific international seminars, national conferences and workshops for knowledge/Experience sharing.

Members enjoy the benefits of IEEMA representations made at both central and state level to the government and other stakeholders on major issues impacting the electrical equipment industry and with suggestions.

Web address: http://elecrama.com

Email: elecrama@ieema.org

NATIONAL INSTRUMENTS

Participation Criteria and rules:

- The team can have an individual to a maximum of 5 members.
- All the members of the team should be a student of a Registered Educational Institute in India and be graduating in 2018 or later. The contest is not open to individuals who will graduate in the year 2017 or earlier.
- All the members of the team should be enrolled only in an engineering course at the undergraduate level. However, students may form teams from different institutes.
- Teams are allowed to submit only one project entry.
- Finalists will present their project at NI Days in October where winners will be announced.

Benefits for students:

- First Prize: Rs 75,000*
- Second Prize: Rs 50,000*
- Certificates for the winners
- Get your college and team nationwide recognition
- Win a chance to participate in NI Global Student Design Contest.

TEXAS INSTRUMENTS

Pattern: Students will be given 30 Multiple Choice Questions to solve in 60 minutes. Students will be awarded +3 Marks for correct answer and -1 Marks for incorrect answer.

Benefits for Students:

- E-certificate (of participation) to all after submission of Design Report.
- Cash prizes upto 1 Lakh can be won in each contest.
- WEBENCH Ambassador Award.
- Hands-on experience in using WEBENCH® design environments.
- Opportunity to interact with other WEBENCH® design engineers.
- Opportunity to learn and practice efficient design techniques.

Eligibility:

- Students completing their courses in 2016 or later shall only participate.
- Each student must participate individually.
- The participant should create a my.TI Account to work with WEBENCHtool.
- No entry fee for participation.

PLACEMENT PREPARATION

G.PRABHAKARAN(142020 / IV YEAR B SEC)

STEPS FOR PRE PLANNING FOR THE INTERVIEW:

1. SWOT Yourself (Self Evaluation).
2. Prepare your Resume.
3. Brush up your Aptitude Skills.
4. Practice for GD.
5. Prepare for PI.
6. Research Company thoroughly.

Step 1: Build a Right Resume

- Start by reading at least 15 articles on right resume, online, and develop your own understanding of what constitutes a good resume.
- The layout must be clean avoid unnecessary clutter, fancy fonts or colours. Keep the text sharp by editing out unnecessary words but at the same time highlighting your strengths. Choose your words strategically.
- Projects, training and internships must be described in such a way that they reflect your contribution. Highlight extracurricular activities where you excelled over others

Step2:Know the Opportunities

Talk to your TPO and keep abreast of companies visiting the campus, the roles on offer and their selection processes. Some companies may cancel their visits at the last minute and some new ones may be roped in. Keeping track of these developments as they happen could prove to be vital.

Step 3: Practise Interviewing

And then, practice some more! Do the same for your campus interview performance:

a. Read all you can about interviews, by logging on to the Internet.

b. Create, collate or download a list of common interview questions.

c. Next, think about the interview questions: strengths/weaknesses, long-term/short-term career goals, what motivates you, skills, career interests. Develop and answer your list of questions.

d. Get someone with industry experience (and of course, the willingness to help), to review your answers. Revisit your answers and incorporate whatever feedback makes sense.

Step 4: Review curriculum

- For instance, for an IT job, data structures, sort algorithms and basics of C/C++ would generally suffice.
- Similarly, a VLSI design job might require basic knowledge of digital circuits, Boolean algebra, electronic systems design and finite state machines etc.
- If you are clueless about the kind of questions the interviewers will ask, then be ready with a list of three to four 'favourite subjects'.

- Some companies try to test how good you are in your strength areas and select you on the basis of that knowledge even if that is not relevant to the job profile on offer.

HOW TO PREPARE

- In your spare time, solve a good puzzle book (Shakuntala Devi's, RS Agarwal's are a good bet).
- Practise with CAT preparation material for aptitude tests and test papers of PSUs (BPCL, BHEL, NTPC etc).
- Solve papers completely (especially those of companies planning to visit your campus) in strict exam conditions, to get a first-hand feel of the level of difficulty and the desired speed. Google search is another good source of question papers.
- Ensure that you solve each problem you gather at least once, including the ones you couldn't solve while you were simulating the actual test.

Step 6: Research companies and industries

Surf the Internet for information regarding the company - history, locations, main products/services, and for any major news story in the past two to three months. Also read about the industry-major players, industry history, major challenges, trends and future direction. Knowing the company and industry, adds credibility when you say you want to work there. Being well-informed reflects interest, a potential to become productive early, and also one's ability to make a sincere effort.

Step 7: File all certificates

Get together all your educational/ non-education certificates including Class 10 and Class 12 mark sheets, technical certifications, the one that you received for NCC/ NSS certificates every documentary proof of achievement so far. Arrange them neatly in a file folder; have them at hand during your interview.

CAMPUS DRIVE TILL AUGUST'17 FOR 2018 BATCH

S.SARAVANAKUMAR(142046/ IV YEAR C SEC)

1) **Chargebee** : (02-08-2017)
   10 th, 12th, college - no criteria
   (Excellent in coding)
   venue - KLNCE

2) **Aricent** : (03-08-2017)
   10th, 12th - 60%
   college - 65%
   venue - KLNCE

3) **IVTL** : (05-08-2017)
   10th, 12th - 80%
   college- 80%
   venue- TCE

4) **Ampisoft** : (10-08-2017)
   10th, 12th - no criteria
   college - (Excellent in coding)
   venue - SIT

5) **Vuram** : (11-08-2017)
   10th, 12th- 70%
   college - 75%
   venue - KLNCE

FUNDINGAGENCY

S.SOWMIYA(152022 III YEAR / B SEC)

Funding is the act of providing financial resources, usually in the form of money, or other values such as effort or time, to finance a need, program, and project, usually by an organisation or government.

PURPOSE OF FUNDING :

There are many types of fundings are available in INDIA. Now we see about rearchfunding .

Research Funding:

It is used for research, in fields of technology or social science. Research funding can split into commercial and non-commercial. Research and development departments of a corporation normally provide commercial research funding.

Whereas, non-commercial research funding is obtained from charities, research councils, or government agencies. Organisations that require such funding normally have to go through competitive selections. Only those that have the most potential would be chosen. Funding is vital in ensuring sustainability of certain projects.

METHODS OF FUNDING:

Government Grants:

Government could allocate funds itself or through government agencies to projects that benefits the public through selection process to students or researchers.

Crowdfunding:

Crowdfunding exists in mainly two types, reward-based crowdfunding and equity-based crowdfunding.

Raised from investors

To raise capital, you require them from investors who are interested in the investments. You have to present those investors with high-return projects. By displaying high-level potentials of the projects, investors would be more attracted to put their money into those projects.

After certain amount of time, usually in a year's time, rewards of the investment will be shared with investors. This makes investors happy and they may continue to invest further.

LIST OF FUNDING AGENCIES

Ministry of Power

http://powermin.nic.in/

Ministry of Non-Conventional Energy Sources (MNES)

http://www.indiasolar.com/mnes.htm

Defence Research & Development Organisation (DRDO)

http://www.drdo.nic.in

Ministry of Communications & InformationTechnology

http://www.mit.gov.in/

Council for Scientific & Industrial Research (CSIR)

http://csirhrdg.res.in/

Department ofscience & technology

http://www.dst.gov.in/

Indian society for technical education (ISTE)

www.isteonline.in/

The institution of engineers(India)

https://www.ieindia.org/

Ministry of Micro, Small & Medium Enterprises

http://msme.gov.in/

www.msme.nic.

FUND RAISING METHODLOGY

R.SARAVANA KUMAR(142055/ IV YEAR C SEC)

What is required for NGO Project Proposal:

In the project proposal, NGO has to explain the requirement of the area, requirement of the people they are working for betterment and upliftment. More clearly in projects proposal NGO has to explain, describe, clarify, define, illustrate and justify information regarding issues, problems, requirement to fulfil and has to explain for the problems, objectives, purpose, plan, motive, justified reasons, requirements to get funds for betterment and to improve and promote the beneficiaries for resolving, achieving and implementing the plan.

What is required for Government Project Proposal:

The priority areas are given separately for each division for R&D funding. However, projects with exceptional merit may also be considered, in non thrust areas. Well-defined goals, milestones, targets and deliverables should be specified clearly. In view of rapid rate of obsolescence, the project duration should preferably be small. Projects aimed at developing closely guarded technologies which are of significant economic and strategic advantage to the country would be given preference. The implementing organizations should normally have the basic infrastructure and capabilities to carry out the project. Building, air conditioning & other infrastructural facilities etc. are not considered.

How to write Project Proposal in Perfect way to get ensured Funding

The proposal writing approach matters to express and describe about your vision, requirement, your ability, capability, specify the issues of the area about the project is concerned and more specifically it represent the profile in proper. The Project must be include with the exact requirement of the working area and proposed topic for which you are applying. You must ensure to build the credibility to extend the approach in obtaining the required grant. There are some certain time limits to get funding from various Government Ministries, Funding agencies and Funding Provider Groups including the companies who have to provide fund **Corporate Social Responsibility** (CSR). The basic parameter is the budget limit of grant maker group. Along with this there is certain area and issue specific predefined or focused parameters are there on which they have to or wish to approve and provide funds to deserving NGOs. So before applying you must check these parameters and limitations. One of the most important factor ge get approval of grant fund is the profile of applicant organisation.

Funding agencies

Ministry of Micro, Small & Medium Enterprises

Micro, Small and Medium Enterprises (MSME) sector has emerged as a highly vibrant and dynamic sector of the Indian economy over the last five decades. MSMEs not only play crucial role in providing large employment opportunities at comparatively lower capital cost than large industries but also help in industrialization of rural & backward areas, thereby, reducing regional imbalances, assuring more equitable distribution of national income and wealth. MSMEs are complementary to large industries as ancillary units and this sector contributes enormously to the socio-economic development of the country.

Eligible credit facilities:

The credit facilities which are eligible to be covered under the scheme are both term loans and/or working capital facility up to Rs.100 lakhs per borrowing unit, extended without any collateral security and/or third party guarantee, to a New or Existing Micro and Small Enterprise. Any credit facility in respect of which risks are additionally covered under a scheme, operated by Government or other agencies, will not be eligible for coverage under the scheme.

Infoview
Fueling Thought, Igniting Action

IVTL
CAMPUS DRIVE

S.VIVEKRAM (142002/ IV YEAR C SEC)

IVTL INFOVIEW OFF CAMPUS DRIVE | TRAINEE ENGINEER | 2018 BATCH

About The Company :

Infoview is a Global IT Services organization with proven expertise in Application Maintenance Outsourcing, Infrastructure Management Services, Enterprise Applications, Product Life Cycle Management and Custom Software Development. Established in 2000 in Chennai (India), for over a decade, it provided consulting, development and maintenance of Enterprise Applications for major Japanese customers. Honed with its rich experience of working across culture and language barriers, Infoview is currently committed to offer its services to customers across the globe.

Company Website : www.ivtlinfoview.com

Job Location : Chennai, Trichy.

Position : Trainee Engineer**Salary :** 6.5 LPA

Date: 06 August 2017.

Interview Location : Thiagarajar College of Engineering,Madurai.

Eligibility Criteria :

- Batch : 2018
- Percentage Criteria : Minimum 80 % is required in 10th,12th and 80% in UG .
- No Diploma Candidates (lateral entry).
- No backlog at the time of attending the interview and while joining.

Skill Set :

- Should have good communication skills.
- Knowledge in Oops concepts, SDLC
- Knowledge on Core Java, XML, CSS
- Ability to write code in any language – C, C++, Java or other languages.
- Final year project using Programming is an add ed advantage.**Selection Process :**

Pre Placement Presentation

- Written Test (Online) – Aptitude & Java basics.
- Technical Programming Test (Online / Offline).
- Communication Round (Oral).
- Technical Interview (Face to Face – Two rounds)
- Final Interview with CEO (Optional)

KLNCE EEE DEPARTMENT PLACEMENT DETAILS

LIST OF PLACED STUDENTS OF 2017 BATCH TILL JULY'17

2013- 2017 Batch – PLACED STUDENTS Details

Detail	No. of Students
Total number of students	120
Number of Students placed as on July'17	56

S.NO	NAME OF THE STUDENT	NAME OF THE COMPANY	MODE OF PLACEMENT
1	Rishikesh T K	ZOHO Corporation, Aspire Systems, Innovative Invaders Technologies Lucid Image & Mind Tree	ON Campus
2	Vanitha S R	Fleet Studio, Ethnus&Solaritis	ON & OFF Campus
3	Subashini R	Fleet Studio, FACE &Chella Software	ON & OFF Campus
4	Soma Sundaram D	Data Pattern, Vuram Technology, TESSOLVE Semiconductor Pvt Ltd& Lucid image,	ON Campus & OFF Campus
5	Vaithee Subramanian	Data Pattern & TESSOLVE Semiconductor Pvt Ltd	ON Campus & OFF Campus
6	Santhana Krishnan T L		
7	Puvaneswaran M	Data Pattern	ON Campus
8	Parthasarathy G	Vuram Technology	ON Campus
9	Aadhithya S	Lucid Image	ON Campus
10	NoorjahanRofina K		
11	Varshitha S		
12	Dinesh Kanna B	Just Dial & IDBI	OFF Campus
13	KarthikVikram K G K	IVTL	ON Campus
14	Senthil Kumar K	Adroit Soft India Pvt Ltd	OFF Campus
15	Pavithra R	TESSOLVE Semiconductor Pvt Ltd	ON Campus & OFF Campus
16	Praveen Kumar S	Chainsys , Southerland & IDBI	ON Campus
17	Hema R	Vee Technologies	ON Campus
18	Boomathi M		
19	AmbrishBabu	Innovative Invaders Technologies & Vuram technology	ON Campus
20	Aravind J R	Indaid Engineering Pvt Ltd	ON Campus
21	Parameshwari.M		
22	SuriyaPandian.K	FACE & IDBI	ON Campus
23	Priyanka R R		
24	Yogesh.M	Amphisoft, Abiba Systems	ON Campus & OFF Campus

S.NO	NAME OF THE STUDENT	NAME OF THE COMPANY	MODE OF PLACEMENT
25	A.G.Sanjeev Kumar	Southerland, Sharekhan& ICICI Securities	OFF Campus
26	A.Vanmathi	Southerland	OFF Campus
27	Venkatsabari K K	IDBI	ON Campus
28	Periyanayaki		
29	Sasikumar M		
30	Mohammed faizal		
31	Sasikumar D		
32	Coleen S		
33	Priyanka S	IDBI & Southerland (Non voice)	ON Campus & OFF Campus
34	Dharani P		
35	Dinesh M		
36	Sowendarya S	Ethnus, Southerland (Non voice) & Tech Mahindra	ON Campus & OFF Campus
37	Ramkumar P	Ethnus	ON Campus
38	Indira G	Amazon & Jasmine Infotech	OFF & ON Campus
39	Kaleeswari M	E-Care Solution	OFF Campus
40	Karthick S	BharathiAxa Insurance	OFF Campus
41	Packia Lakshmi D	Southerland (Non voice)	OFF Campus
42	Lavanya R		
43	SrinithiTrishna V		
44	Shanthini G		
45	Suresh Kumar U		
46	ArunPandian S		
47	Kowsalya M	Hinduja Global Solutions	ON Campus
48	AravinthanSamy A		
49	Manikandamariappan A		
50	Kathirvel M		
51	Snegajan S C	Tech Mahindra	ON Campus
52	Syed Abdullah S		
53	Vineeth Kumar R	SRS Niagara Energy Saver	OFF Campus
54	Kavitha S	Solver Minds	ON Campus
55	Radhika S		
56	Selvaraj U	Solarits	OFF Campus

Total Number of students placed: 56
Total Number of Placements: 85

AWARDS RECEIVED BY
EEE STUDENTS 2016-2017

NAME	YEAR	TITLE OF EVENT	DATE	LOCATION	PRIZE WON
1. Kowsalya. M 2. Kowsalya. M 3. Kavitha. S	IV	Technical Colloquium – EVATAR 2K16 (Connection)	09.08.16 & 10.08.16	Kamaraj college of Engineering & Technology, Virudhunagar.	I
4. Kavitha. S 5. Kowsalya. M 6. Kowsalya. M	IV	Technical Colloquium – EVATAR 2K16 (Project parade)	09.08.16 & 10.08.16	Kamaraj college of Engineering & Technology, Virudhunagar.	III
7. Kavitha. S	IV	Technical Colloquium – EVATAR 2K16 (Device Tracking)	09.08.16 & 10.08.16	Kamaraj college of Engineering & Technology, Virudhunagar.	I
8. Saravana Kumar R	III	Founders day Centenary Celebration (Class Topper Award)	31-12-16	KLNCE, Pottapalayam.	I
9. N.Aishwarya	II				
10. T.K.Rishekesh	IV	Texas Instruments WEBENCH Design Contest	Dec 2016	KLNCE, Pottapalayam.	I
11. R.Saravanakumar 12. V.Suriyakumar 13. S.Vivekram 14. M.Sathishkumar	III	Project contest	15.02.17	Velammal college of Engineering & Technology, Madurai.	II

NAME	YEAR	TITLE OF EVENT	DATE	LOCATION	PRIZE WON
15. V.Suriyakumar 16. S.Vivekram 17. R.Saravanakumar	III	Project display	04.03.17	Madras Institute of Technology, Anna university, Chennai.	II
18. G.Parthasarathy 19. T.K.Rishekesh 20. Mageshwaran.S	IV	Project contest	15.02.17	Velammal college of Engineering & Technology, Madurai.	III
21. M.Sathishkumar 22. S.Vivekram 23. V.Suriyakumar 24. R.Saravanakumar 25. B.Abinaya 26. M.Jananie 27. A.AlifRoja 28. S.BagamPriyal	III	Project Expo	17.03.17	KLNCE, Pottapalayam,	II
29. A.R.Yuvaraj Srinivasan 30. R.R.Sathishkumar 31. Vishnuvardhan	III	Crazieelectronics	18.03.17	Thiagarajar college of Engineering, Madurai.	I
32. A.R.Yuvaraja Srinivasan	III	Error-404	17.03.17 & 18.03.17	Kalasalingam university, Krishnankoil.	III
33. K.Anandraj 34. R.Aravindasamy 35. B.Ajith	II	Treasure Seeker	18.03.17	Thiagarajar college of Engineering, Madurai.	I
36. J.MeribaCecili 37. T.Muthumathi 38. P.MeenaPriya Dharshini	III	National level project contest	28.01.17 & 29.01.17	ISSRD-ESIC 2017	Best project award
39. HariPreethi	III	TI Contest – DrishTI	28.12.16	K.L.N. College of Engineering, Pottapalayam.	I

STUDENTS PARTICIPATED IN CONFERENCE & JOURNAL 2016-2017

NAME OF THE STUDENT	YEAR	TITLE OF THE PAPER	CONFERENCE/ JOURNAL DETAILS	VENUE	DATE
1. T.L.Santhanakrishnan 2. S.Syed Abdullah 3. M.Yokesh	IV	Smart hybrid energy management using solar power.	TEQIP-II sponsored International conference on contemporary topics in power Engineering and aiding technologies	Pondicherry Engineering college, Puducherry.	25.02.17
4. S.Varshitha 5. G.Shanthini 6. G.Sivasakthi	IV	Controlling building fire and smoke evacuation using IoT.	National conference	P.S.R Engineering college, Sivakasi.	10.03.17 & 11.03.17
7. R.Priyanka	IV	Optimal allocation and sizing of DG in a radial distribution system using WHALE optimization algorithm.	IEEE International conference.	Dr.N.G.P Institute of Technology, Coimbatore.	16.03.17 to 18.03.17
8. T.G.Meena 9. S.Prathibah 10. M.Kowsalya (132069) 11. M.Kowsalya (132012)	IV	Power quality improvement using energy storage device.	International conference	Christian college of Engineering and Technology. Dindigul.	15.03.17 to 17.03.17
12. K.Karthick 13. M.Manikandan 14. S.Nizharudeen 15. N.N.Karthikeyan	IV	Voltage regulation on transmission line using PV system.	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
16. A.MohammedFaizal 17. K.Manojkumar 18. MP. LaxmenBabu 19. P.Naveenkumar	IV	Remote wide area oscillation monitoring and load shedding using load restoration method	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
20. B.N.Padmanaban 21. S.Muthukumar 22. A.Manikanda mariappan 23. M.Monish	IV	Smart sensed automatic control of boiler using PID controller	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
24. T.Muthumathi 25. J.Meribacecili	IV	Controlling of national grid using SSFCL	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17

26. S.Priyanka 27. M.Parameswari 28. Ponsarumathi	IV	Energy efficient BLDC drive with constant torque using C-DUMP technology.	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
29. R.R.Priyanka 30. M.Perianayagi 31. S.Navaneetha	IV	Optimal allocation of DG with SVC in meshed transmission system	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
32. A.Romika 33. S.Radhika 34. R.Pavithra	IV	Optimal allocation and sizing of DG in a distribution system using MOTH swarm optimization	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
35. R.Lavanya 36. K.Noorjahanrofina 37. D.Packialakshmi	IV	Voice for voiceless using flex sensor and arduino	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
38. S.Kavitha 39. M.Boomathi 40. S.Coleen	IV	Design and implementation of PV based harmonic filter using LabVIEW	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
41. Aravinthansamy 42. Dhanasekaran 43. Dineshbabu	IV	Automatic solar tracking and measuring using PIC microcontroller	National conference on recent trends in networking, computing and communication	PSNA College of Engineering and Technology, Dindigul.	27.03.17
44. K.Veerapathiran 45. K.Senthilkumar	IV	Attendance Monitoring system for institution using Rasperry Pi	National conference on signal processing, communication and Networking	PSNA College of Engineering and Technology, Dindigul.	24.03.17
46. T.Muthumathi 47. P.Meenapriya dharshini	IV	Microgrid controlling using TBSSFCL	TEQIP II sponsored International conference on contemporary topics in power engineering and aiding technologes	Pondicherry Engineering college, Puducherry	25.02.17
48. A.Ariharan 49. P.Balaviknesh 50. M.Kaleeswaran 51. T.Kannusamy	IV	Arduino based Home energy management system	National conference – INSTRUVISION'17	Kamaraj college of Engineering & Technology, Virudhunagar.	31.03.17

STUDENTS PARTICIPATED IN PROJECT CONTEST 2016-2017

S.No.	NAME	YEAR	TITLE OF EVENT	DATE	LOCATION
1. 2. 3.	Ramkumar. P Rishekesh. T K AmbrishBabu S	IV	Project Contest-8th India Innovation Initiative- i3 National fair 2016	19.10.16	AICTE, The Lalit New Delhi
4. 5. 6.	Nandha Kumar S Ajith P Gokul S M K	II	Project contest (Wireless Notice board) IPC 2K16	16.09.16	KLNCE Pottapalayam.
7. 8. 9.	S.AmbrishBabu P.BalaMurrugan L.Harishankar	IV	Intercollegiate IEEE Project Contest	15.02.17	Velammal college of Engineering & Technology, Madurai
10. 11. 12. 13. 14. 15. 16. 17. 18. 19. 20. 21. 22. 23. 24. 25. 26. 27. 28.	J.R.Hariharasudhan S.Velammal K.Vasumathi P.Krishnavenishri S.Karthikasundari C.Madumitha G.Bino P.Meenambigai R.R.Sathishkumar J.G.Vishnuvardhan K.Ranjith S.G.Thaiyalnayagi C.K.Vishali S.Ponmanipriya R.S.Nandhini N.Nivetha K.Nivethadevi B.Preethipasri A.R.Yuvaraja Srinivasan	III	Intercollegiate IEEE Project Contest	15.02.2017	

S.No.	NAME	YEAR	TITLE OF EVENT	DATE	LOCATION
29.	S.K.Ramya	III	Mini project contest	26.08.16	KLNCE Pottapalayam.
30.	T.Malathi	III	Project contest	15.02.17	Velammal college of Engineering & Technology, Madurai.
31. 32. 33.	M.Kaleeswaran A.Ariharan T.Kannusamy	IV	Intercollegiate IEEE Project Contest	15.02.17	Velammal college of Engineering & Technology, Madurai.
34.	A.R.YuvarajaSrinivasan	III			
35.	L.H.Praveenkumar	IV			
36. 37. 38. 39. 40. 41. 42. 43.	.L.Omnath K.Rajamanikam C.Venkatesh K.B.Suriyanarayanan J.Venkateshkumar B.Vidhyatharan M.VimalRajasekar K.Yuvalakshman	II			
44. 45. 46. 47.	K.Rajamanickam R.RameshBalaji L.H. Praveen kumar S.L.Omnath	II	IEI-Project Expo	17.03.17	K.L.N.C.E Pottapalayam
48. 49.	S.Priyanka R.Rajapriya	III	IEI-Project Expo	17.03.17	K.L.N.C.E Pottapalayam
50. 51.	S.Kavitha M.Kowsalya	IV	Project contest – SRISHTI 2017	13.02.17 & 14.02.17	Saints college of Engineering, Kottayam, Kerala.
52. 53.	S.Prathibah T.G.Meena	IV	Project presentation in Avesha 2K17	31.03.17 & 31.03.17	Bharathidasan Institute of Technology, Tiruchirappalli.
54.	S.Nandhakumar	II	Project contest – Data Logging	21.02.17	KLNCE, Pottapalayam

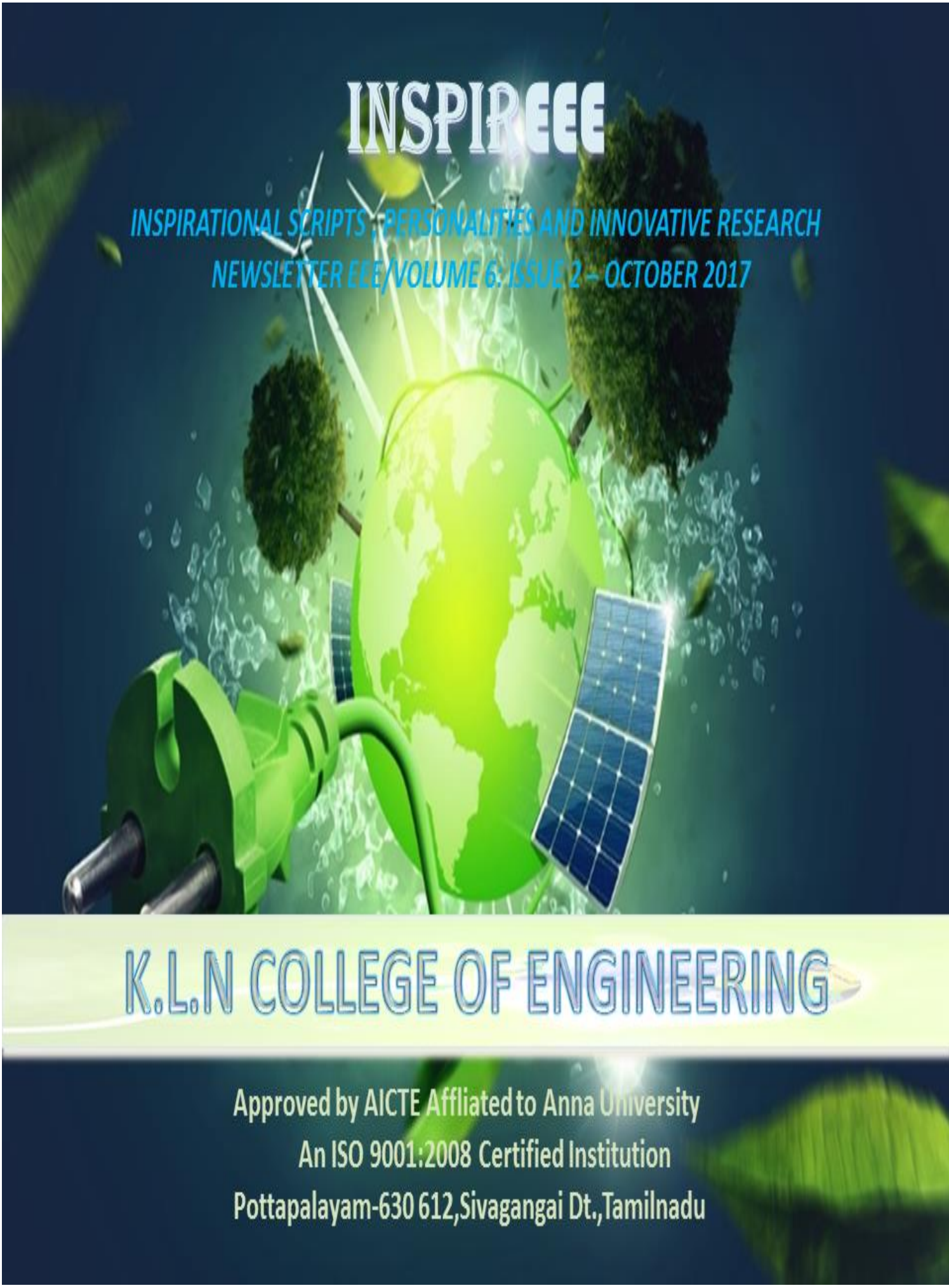
STUDENTS UNDERGONE INPLANT TRAINING 2016-2017

S.No	NAME	YEAR	NAME OF THE COMPANY	PERIOD	LOCATION
1. 2. 3.	L. Hari Shankar K.G.K. KarthikVikram K.S. Ajith Kumar	III	Tamil Nadu TRANSCO Ltd.	04-07-16 to 08-07-16	Pasumalai, Madurai.
4. 5.	Nandha Kumar S Vasanthavikash P	II	KaashivInfotech, Chennai	30-11-16 to 04-12-16	Chennai
6. 7. 8. 9. 10.	RibuHassini S Swathika K ThaiyalNayagi S G Velammal S Vishali C K	III	Tamilnadu Generation & Distribution Co.Ltd	26-12-16 to 30-12-16	Tuticorin Thermal Power Station
11. 12. 13. 14. 15. 16. 17.	KarthikaSundari S Devi Priya V Krishnaveni Shri R Divya B Gowsike S G HariPreethy R Jananie M	III	Tamil Nadu TRANSCO Ltd	28-11-16 to 02-12-16	Pasumalai, Madurai
18. 19.	Malathi T Meenambigai	III	Tamil Nadu Generation and Distribution Corporation Ltd	21-12-16 to 25-12-16	Pasumalai, Madurai
20.	B.Abinaya	III	Tamil Nadu TRANSCO Ltd	13.12.16 to 17.12.16	Pasumalai, Madurai
21. 22. 23. 24. 25. 26. 27.	R.Gayathri S.Bagampriyal M.Keerthana B.Preethipasri R.S.Nandhini S.Ponmanipriya S.K.Ramya	III	Tamil Nadu Transmission Corporation Limited.	12.12.16 to 17.12.16	P.P.Colony,Madurai
28.	Meharunnisha S	II	BSNL, Madurai	05-12-2016 to 09-12-2016	BSNL, Madurai.

S.No	NAME	YEAR	NAME OF THE COMPANY	PERIOD	LOCATION
29. 30. 31.	M.Kowsalya M.Kowsalya S.Kavitha	IV	E.I.D. Parry (India) Limited	05.12.16, 09.12.16 & 10.12.16 (3days)	Sivagangai
32. 33. 34. 35. 36. 37.	Syed Abdullah M.Veeralakshmi V.Vivitha S.R.Vanitha M.Vigneshwari A.Vahithasulthana	IV	Advanced training Institute, Chennai	05-12-16 to 09-12-16	ATI, Guindy, Chennai
38. 39.	S.RibuHassini K.Swathika	III	Tamil Nadu Generation & Distribution co.ltd	26-12-16 to 30-12-16	Tuticorin Thermal Power Station
40.	K.Nevadhaa Devi	III	Tamil Nadu Transmission Corporation Limited	12-12-16 to 17-12-16	P.P.Colony, Madurai.
41.	R.Saravanakumar	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 08.06.17	Pasumalai, Madurai.
42.	M.Vigneshwaran	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
43.	V.Suriyakumar	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
44.	S.Vivekram	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
45.	K.Vadivarasan	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
46.	B.Suriyaprakash	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
47.	M.Sudharsan	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
48.	G.Prabhakaran	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
49.	R.Pradeep Kumar	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.

S.No	NAME	YEAR	NAME OF THE COMPANY	PERIOD	LOCATION
50.	S.Saravanakumar	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
51.	M.Sathish Kumar	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
52.	M.Ponbalamurugan	III	Tamil Nadu Generation & Distribution co. ltd	05.06.17 to 09.06.17	Pasumalai, Madurai.
53.	S.Nandhakumar	II	Railnet software solutions	12.06.17 to 17.06.17	Railway colony Madurai
54.	L.H.Praveenkumar	II	Railnet software solutions	12.06.17 to 17.06.17	Railway colony Madurai
55.	K.B.Surya Narayanan	II	Railnet software solutions	12.06.17 to 17.06.17	Railway colony Madurai
56.	C.K.Vishali	III	Tamil Nadu Transmission corporation limited	06.06.17 to 10.06.17	Pasumalai, Madurai.
57.	S.Velammal	III	Tamil Nadu Transmission corporation limited	06.06.17 to 10.06.17	Pasumalai, Madurai.
58.	B.G.Vivetha Sri	III	Tamil Nadu Transmission corporation limited	06.06.17 to 10.06.17	Pasumalai, Madurai.
59.	K.YogaRajika	III	Tamil Nadu Transmission corporation limited	06.06.17 to 10.06.17	Pasumalai, Madurai.


VISION
To become a high standard of
Excellence in Education,Training
And Research in the field of
Electrical&Electronics Engineering
And allied areas
time to
go green
MISSION
To produce excellent,innovative
And Nationalistic Engineers
With Ethical Values and to
Advance in the field of Electrical
&Electronics Engineering and
Allied areas


INSPIREEE
INSPIRATIONAL SCRIPTS , PERSONALITIES AND INNOVATIVE RESEARCH
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K.L.N COLLEGE OF ENGINEERING
Approved by AICTE Affliated to Anna University
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Pottapalayam-630 612,Sivagangai Dt.,Tamilnadu

INSPIREEE

INspirational **S**cripts, **P**ersonalities and **I**nnovative **R**esearch of **EEE**

VISION

To become a high standard of excellence in Education, Training and Research in the field of Electrical and Electronics Engineering and allied applications

MISSION

To Produce excellent, innovative and Nationalistic Engineers with Ethical values and to advance in the field of Electrical and Electronics Engineering and Allied Areas

K.L.N. College of Engineering

Pottapalayam – 630 612, Sivagangai District, Tamil Nadu, India

INDEX

MESSAGE FROM HEAD OF THE DEPARTMENT

Greetings,

In this issue, various technical competitions, organized by companies like TCS, NI, TI and ELECTRAMA etc., briefed. This gives some insight into the level of technical competency to be attained, how to prepare to face such competition explained, in a systematic manner. Participation in such technical competitions will not only improve the technical competency, give an opportunity to become a part of such associations, by getting internship / inplant training. These opportunities are knocking our doors, and it is upto as to develop an attitude to make use of it.

Final year students who have attended Campus recruitment, shared their experience in this issue. How to prepare for Campus recruitment, depends on company. The requirements of companies like Aricent, Infoview, etc. nicely explained by the students.

This issue also brings out students participation in various events like workshop, conference, journal publication, placed student details, inplant training, etc. This is to motivate the second and third year students, to regularly participate in such academic activities, to update themselves to facilitate good progress.

Best wishes

Dr. S.M. KANNAN

Head of the Department - EEE

EDITORIAL CREW

EDITOR IN-CHIEF:

Dr. S.M. KANNAN [Professor & Head]

EDITOR:

Dr. S.P. RAJARAM [Assistant Professor 2]

STUDENT IN-CHARGE:

SURIYAKUMAR V(142006 / IV Year C Section)

SATHISHKUMAR M (142052/ IV Year C Section)

ALIGNMENT & CORRECTION:

PONBALAMURUGAN M (142303 / IV Year B Section)

VIGNESHWARAN M (142005 / IV Year C Section)

POSTER CREATION:

THAMODHARAN P (152904 / IV Year C Section)

NIYANTRA 2017

PRIYANKA. S (142003) (IV YEAR/ 'C' SEC)

About NIYANTRA

NIYANTRAisanannualStudentDesigncontestorganizedbyNationalInstruments.Thepurposeofthecontestistoenable students to come up with engineering systems or products that solve some challenging, real-world problems. One such platform-based approach is the National Instruments Graphical System Design, enabled by the LabVIEW RIOArchitecture.

Participation Criteria & Rules

1) The team can have an individual to a **maximum of 5members**.
2) All the members of the team should be a student of a Registered Educational Institute in India and be **graduating in 2018 or later**.
3) All the members of the team should be **enrolled only in an engineering course at the undergraduate level**. No postgraduate students (M.E/M.Tech/M.Sc/Doctorate) areallowed.
4) A student **cannot be a member of multipleteams**.
5) The team needs to have **one faculty member** from their college/university as theirguide.
6) Teams are allowed to **submit only one** Projectentry.

How to Submit the Abstract:

The teams will be able to submit their abstract during **April 03 2017 – May 19 2017** byvisiting india.ni.com/niyantra

- The abstract submission will be done through a form with multiple fields. The teams will have to complete the abstract submission by filling up all of these fields. **This form will only be available during April 03 2017 – May 192017.**
- No abstracts will be accepted through e-mail. Any team submitting an abstract other than the designated form will bedisqualified.
- Only one abstract per team is allowed.

Contest Stages & Description

Step 1) Abstract Submission (Deadline: May 19th, 2017)

Formateamofmaximum5studentsandsubmittheabstractofyourproject(makesure youfollowthestepsforsubmitting the abstract).

***Step* 2) Online Training(Duration: Second week of June, 2017*)**

Once you are short-listed, you could attend an online web-based training. The training will be free of cost and you will be allotted mentors for guidance.

***Step 3*) Progress report(Deadline: July 10, 2017*)**

After the training period, each team is required to send in the progress report on the initial abstract. This is a mandatory step after training.

***Step 4)* Viva(Duration: Third week of July, 2017*)**

AfterthesubmissionofProgressreport youwillalsoberequiredtoselectslotsforViva.Theseslotswillbeallottedonfirst- come-first-servebasis.

***Step 5)* Project Completion(Duration: August-September, 2017*)**

Afterthevivaround,teamswillbeshortlistedforthe Nationallevel.Allshortlisted teamswillthenhave45daystocomplete theirprojects.

Step 6) Final Interview(Duration: First/Second Week of October, 2017*)

Teamswillberequiredtosubmittheirfinalreportalongwithavideooftheircompletedproject.

***Step 7)* Presentation at NI Days(Deadline: October, 2017*)**

After the final shortlist, the finalists will present their applications at NI Days 2017 and the winners will be announced.

TI INNOVATION CHALLENGE

THINK! INNOVATE!!

K.VISHALI(142033) IV year/ C sec

ABOUT:

Texas Instruments Inc. in collaboration with Department of Science and Technology (DST) proudly announce the 'DST & Texas Instruments Inc. India Innovation Challenge Design Contest 2017', Anchored by the Indian Institute of Management (IIM), Bangalore and supported by MyGov. The contest is open for all students pursuing B.E./B.Tech, M.E./M.Tech & Ph.D from Indian engineering colleges. This contest is for students from Indian engineering colleges who have a dream to create something new, aspire to make a difference and contribute to India's success towards becoming an innovation hub.

TI PRODUCTS: TI technology is at the heart of all things electronic. We focus on developing **analog chips** and **embedded processors**, which account for more than 80 percent of our revenue. After all, there isn't an electronic device on the planet that doesn't require an analog chip and most require an embedded processor.

CONTEST STRUCTURE:

TEAM

- 2 to 5 members per team, one of them as Team leader, rest are Team member.
- As an option, team can add one of their faculty as a mentor.
- Team can be formed out of mix of different departments.
- All Team members should belong to the same institution.

MILESTONES

- Qualifying round – Phase 1
- Qualifying round – Phase 2
- Quarter finals
- Semi finals
- Finals

ELIGIBLITY CRITERIA:

- The contest is open for all Indian engineering students pursuing undergraduate, postgraduate&Doctoral degrees with Indian engineering colleges.
- All the participants must be Indian Nationals.
- TI will be providing TI tools worth up to USD 200 for each team free of cost. You must use all TI devices in your solution.

CONTEST – RULES:

• Participants must use only those **devices** manufactured by **Texas instrument (TI)**

• Pure software solutions are not permitted, **Hardware is a must**

• **"Business Potential"** and **"Technical Innovation"** are the basis on which proposal evaluation will be carried out.

AWARDS AND FUNDING:

– Product Development fund of 5 LAKHS, for 30 teams, amounting to 1.5 crores

– Seed fund of 20 LAKHS, for 10 teams, amounting to 2 crores

– Chairman's award of 7 LAKHS for the contest winner

– Team "1st Runner up" will get 3.5 LAKHS

– Team "2nd Runner up" will get 1.5 LAKHS

– Best of the "Women Team" will get 1 LAKH

– Certificates from TI & NSRCEL, IIM Bangalore

– Participation Certificate in Qualifying Round & Quarter finals

– Certificate of Excellence in Semi finals and Finals

– Certificate after Incubation at NSRCEL, IIM Bangalore.

PARTICIPATION LINK:

www.ti.com/iicdc

PLACEMENT PREPARATION

S.SHEEBA JOSELINE(142021 / IV YEAR C SEC)

PREPARE RESUME

Very first step should be to prepare resume. According to me resume should be simple, impressive and point wise. But make sure you have knowledge of all things that you have mentioned in your resume. Further it is advisable to use different resume for different interviews based on the company. Another important point is to update your resume regularly.

APTITUDE

One of the most commonly used test to short list the candidates in the first round is aptitude. Aptitude includes quantitative aptitude, logical reasoning and verbal. Go through the important concepts of aptitude which asked frequently. Practice via online tests.

WORKOUT ON YOUR BASICS

It is advisable to prepare your basics as strongly as possible as most of the questions connected with them only. The basic idea behind mentioning this point is to let you know that interviewer will check your basic knowledge. So just be sure and confident with your answers and your basics.

EFFECTIVE COMMUNICATION

Good communication skills are necessary for successful campus placement. You need to have good reasoning skills and ability to think logically in your core areas. You should be bold to face the panel of interviewers. During interview, answer should be given in the language they are asked.

STUDY ABOUT THE COMPANY

It is a very common but important topic. Before going for interview or campus placement find some information about the company because they will ask questions related to their company. It is very important to study the profile and business activity of the company from the company website or from any business portal/magazine.

TRICKY QUESTIONS

You may have to face few tricky questions from the interviewer. Few common and frequently asked questions are told about yourself. This is going to reflect you to the interviewer so you have to prepare properly. The questions can be strengths and weaknesses, your hobby, how much is your salary expectation, how do you see yourself in 5 years from now and so on. So prepare the answer of these questions well in advance.

AVOID OVER CONFIDENCE

Over confidence can be really harmful for your campus placement or interview for any other position. So it is better to avoid over confidence. For this it is very important that you must know your strength and weaknesses very well. Proposal it should be plainly plot the requirement of the area with the justification and process of the implement the objectives/issues. Most Funding Organisations have clear pointers, directions and guides on their funding procedures they' focus at a particular bunch, for a specific purpose/issue an area.

FUNDING AGENCY

National Science Foundation

J R Hariharasudhan (142106)IV year/A sec

Introduction:

The **National Science Foundation** (**NSF**) is a United States government agency that supports fundamental research and education in all the non-medical fields of science and engineering with an annual budget of about US$7.0 billion, the NSF funds approximately 24% of all federally supported basic research conducted by the United State's colleges and universities. The NSF's director and deputy director are appointed by the President of the United States, and confirmed by the United States Senate, whereas the 24 presidentially appointed members of the National Science Board.

Scope:

The NSF's workforce numbers about 1,700, nearly all working at its Arlington headquarters. That includes about 1,200 career employees, 150 scientists from research institutions, 200 contract workers, and the staff of the National Science Board office and the Office of the Inspector General.

Programs:

The NSF has launched a number of projects that coordinate the efforts of experts in many disciplines.

- Nanotechnology
- The science of learning
- Digital libraries
- The ecology of infectious diseases.

Understanding:

NSF surveys of public attitudes and knowledge have consistently shown that the public has a positive view of science but has little scientific understanding. The greatest deficit remains the public's understanding of the scientific method.

INDIAN NATIONAL ACADEMY OF ENGINEERING (INAE)

M. JANANIE, (EEE –A, IV YEAR)

ABOUT INAE:

The Indian National Academy of Engineering (INAE) founded in 1987 comprises India's most distinguished engineers, engineer-scientists and technologists covering the entire spectrum of engineering disciplines, and functions as an apex body to promote and advance the practice of engineering and technology and the related sciences and disciplines in India and their application to problems of national importance. The Academy also aims at presenting research and development activities in engineering at academic and professional forums in India and abroad. INAE encourages inventions, scientific investigations and research, and promotes their application for development of all sectors of the national economy.

ENGINEERING EXCELLENCE AWARDS:

1. **LIFE TIME CONTRIBUTION AWARD IN ENGINEERING:**

   The purpose of the award is to recognize the life time contribution in engineering made by eminent persons to any branch of engineering within the purview of the academy. They offer two awards per year and the nominations should be given before 15th May each year. Each nomination is valid upto 3 years and re-nominations are also accepted. The award is of Rs. 5 Lakhs with a citation.

2. **INAE OUTSTANDING TEACHERS AWARD:**

   The award aims to recognize and honour teachers in Indian Colleges, Universities, and Institutions, who have provided guidance and inspired students to take up careers in Engineering and Technology. All disciplines of Engineering and Technology will come under the purview of this award. There will be a maximum of 2 such awards per year. The awardees shall receive a citation, a cash award of Rs. 1 lakh and a onetime book grant of Rs.25, 000/-. The nominee should be working or have worked in India for a minimum of 10 years, and have at least 20 years of teaching experience at undergraduate or postgraduate level in any College/ University/Institution.

3. **INAE YOUNG ENGINEER AWARD:**

   The purpose of the award is to recognize the outstanding contributions and achievements made by Young Engineers in any branch of engineering. The eligibility for this year is, the

nominee should be the citizen of India whose age is not more than 35 years with respect to 1st January 2017. A nomination will be valid for 1 year and a candidate can be nominated for successively 2 years (age not more than 35). Work submitted for award must be carried out totally in India. The Award consists of Rs. 1 Lakh with a citation.

4. **INAE INNOVATOR ENTREPRENEUR AWARD:**

This award is to encourage and recognize the innovation and entrepreneurship among Young Engineers. The engineering innovations/inventions/concepts that have been actually realized and implemented in industry either in new processes or products would be given weightage. The award carries **a cash prize of Rs 2 lakhs** and the awardees would be conferred the same during the Awards Function to be held during the Annual Convention being held in December, each year.

5. **INNOVATIVE STUDENTS PROJECT AWARD:**

This award is to identify innovative and creative research projects undertaken by the students and scholars in the engineering institutions, and to give an early recognition and incentives to them.

The eligibility is,

Potentially innovative projects/theses are assessed at three levels, viz., B.E./B.Tech, M.E. / M. Tech and Ph.D. Nominations for the awards 2017 will be accepted by the Academy according to the following eligibility requirement.

(i) B.E./B.Tech projects completed upto June 30 during the academic year 2017-2018. Only Final Year (4th year) projects of students who will be completing their B.E./B.Tech degree requirements by June 30, 2018 are eligible to be nominated for the subject award

(ii) M.E. / M. Tech theses examined from July 1, 2017 upto June 30, 2018 during the academic year 2017 -2018.

(iii) Ph.D. theses examined and accepted/recommended for Award from June 1, 2016 upto May 31, 2018.

It is compulsory that Degree / Provisional Degree Certificate must accompany the nomination proforma.

The Award comprise a certificate and Rs. 10,000/- in cash to each team member of the selected project, subject to a maximum of four team members.

TEXAS INSTRUMENTS

S.VELAMMAL(142019/ IV YEAR C SEC)

CONTEST DESCRIPTION

This TI Innovation challenge 2016 contest (this "Contest") is designed to encourage graduate and undergraduate engineering students to submit design projects using TI technology (each, an "Entry", collectively "Entries"). The term "Entrant" as used in these Official Rules refers to each eligible Individual and/or Team that submits an Entry. Prizes will be awarded to Entrants who submit the best Entries as determined by the judges in accordance with these Rules.

ELIGIBILITY

1. This Contest is open to individual students ("Individual") or teams of up to seven students Team in which all Entrants:
2. Must be registered as full time or part time graduate or undergraduate students in engineering at an accredited college or university.
3. Must submit the name of their professor as part of the entry form which must be submitted by an Individual or Team Leader.
4. May participate on only one design.

CONTEST PERIOD

The Contest begins September 1, 2015 at 12:01 a.m. and will end on May 27, 2016 at 11:59 p.m. Entries submitted prior to or after the Contest Period will not be accepted. Limit one Entry per Individual or Team.

ENTRY REQUIREMENTS

1. All design ideas must incorporate and use three different TI devices, including at least one TI processor and two unique TI analog integrated circuits. All TI processors are acceptable. Analog integrated circuits ("ICs") must come from any of the following categories: (i) data converters; (ii) amplifiers; (iii) power management devices; (iv) interface devices; (v) switches; (vi) RF devices; (vii) temperature sensors; (viii) clocks and timers; and (ix) comparators.
2. All Entries must be in English.
3. The Entry must be the Individual's or Team's original work and must not infringe upon the copyright, trademark, patent, privacy, publicity or other intellectual property rights of any person or entity.
4. All Entries must be submitted through the followingwebsite:(https://e2e.ti.com/group/universityprogram/students/m/students_repository/)
5. Entrants may modify or supplement their Entries at any time prior to the end of the Contest Period.
6. The Sponsor reserves the right to at any time disqualify an Entrant who Sponsor, in its sole discretion, believes has attempted to undermine the legitimate operation of the Contest, has tampered with the Contest or with any Entry or abuses, threatens or harasses other Entrants or the Sponsor. If a Team member is disqualified, the entire Team will be disqualified.
7. All Entrants agree to follow and abide by the design idea requirements and judging criteria
8. Participation in this Contest constitutes an agreement by each Entrant, including all members of a Team, to be bound by these Rules and by the decision of the judges which shall be final and binding.

Categorical Prizes:

- Best use of TI wireless technology
- Most innovative automotive application
- Innovative home automation application
- Best video demonstration
- Best humanitarian impact
- Best chance at commercialization
- Best use of entire TI portfolio
- Most unique concept
- Best environmental impact
- Most effective power solution

SURIYAKUMAR V

EEE, KLNCE

ARICENT INTERVIEW

Computer Based Test:

First round:

Multiple choice questions on verbal, quantitative, logical and Technical (OS, DBMS and DS). It lasts for 70 minutes.

Second round:

Programming: Two simple programs, 1 Data structure program, 3 query.
Eg: Thelogic is save the 10 cows name count its milk rate repeatedly, find the given series in program and arrangements of lines in program like jumbled sentence.It lasts for 80 mins.

Technical HR:

The HR: We need sometime to go through your file.In the meantime can you tell about yourself?
Me : I tell about my schooling and project details.

The HR: Are you interested in programming or electronics?
Me : I'm interested in programming sir.

The HR: What is the complex program you have written in your life?
Me : It's the program namely" Police-Thieves" which I used to write in codevita contest.

The HR: Can you write the snippet of that program?
Me :

```
for(int i=0;i<n;i++){
for(int j=0;j<n;j++){
  for(int k=0;k<m;k++){
   if(a[i][j]=='P'){if(a[i][j-k]=='T'||a[i][j+k]=='T'){if(a[i][j-k]=='T'){
a[i][j-k]=0;b++;                    }
                    else{
           a[i][j+k]=0;  b++;                    }}}}}}
```

The HR: Can you explain the purpose of these program?
Me : This program is to find out the no. of thieves the policemen can able to catch.

The HR: Can you explain the algorithm of these program?
Me : These program checks the theif in the previous and next element of the array if the present element is police by decreasing and increasing the column count by 1.

The HR: What is the sound you observed in the high voltage transmission lines?
Me : That is called corona . Its due to the ionisation of the air surrounds the conductor.

The HR: How to mitigate the corona effect?
Me : Corona can't be controlled. Humidity content in the air reduces corona effect.

The HR: What do you know about series motor?
Me : Series motor's starting torque is very high. Due to these torque, starting current is also high. Hence the series motor must be start with the load to limit the torque.

The HR: What are the applications of series motor?
Me : Hoists, cranes, trolley cars, conveyors, elevators, and electric traction.

The HR: Asks some basic questions on digital electronics.
Me : I can't able to answer.

The HR: You are little bit weak electronics.
Me : I am not weak sir. My interest in electronics is low compared to electricals.

The HR: If you selected in the interview can you develop the knowledge in electronics.
Me : I will try to improve my interest in electronics.

The HR: Thanks for sharing your wonderful time with us.
Me : Thank you sir.

GENERAL HR:

The HR: Tell about yourself:
Me : I am explaining about my long term and short term goals,etc..

The HR: Why did you choose EEE department?
Me : In EEE all the streams like electronics, electrical and programming papers included.

The HR: Asked some questions to check my leadership quality.
Me : I tell the answers (It is to check your attitude) .

The HR: What do you know about Aricent?
Me : I tell about the Aricent which I referred from their website.

The HR: Do you think your skills are enough to compete the present corporate world?
Me : No sir. But I am quick learner and ready to learn based on your requirement.

The HR: Why do I hire you?
Me : As a fresher, I put my full effort to develop the company and myself also.

The HR: Thank you Suriyakumar.
Me : Thank you sir.

NAME : M VIGNESHWARAN
ROLL NO : 142005
BRANCH : EEE "C"

ONLINE TEST

- In the aptitude test, time and work, percentages, profit and loss, distance and trains based problems.
- Coding section consists of logical oriented programs and also consisted of patterning of programs.
- The microprocessor section having simple MP programs.

TECHNICAL ROUND

HR: Tell about yourself
ME: I'm Vigneshwaran…….....

HR: What's your area of interest?
ME: Power system Analysis, Object Oriented Programming Language.

HR: What are the features in the Object oriented programming languages:
ME: Class, Objects, Inheritance, Polymorphism, Data encapsulation.

HR: What is the purpose of Inheritance:
ME: Inheritance is a mechanism in which one class acquires all the properties and behaviours of the parent class.

HR: Explain the Pointer declaration and its concepts:
ME: Pointer declaration:
Syntax:

```
datatype * variable

Int *p;        p = &a;
```

HR: What is polymorphism?
ME: poly means "many" morphism means "forms". It is ability to use the functions in different forms.

HR: List out the some instructions used in microprocessors:
ME: MOV m data
MOV rd m

HR: What is the function of Accumulator:
ME: The accumulator is an 8- bit register. The result of the ALU operation is stored in accumulator.

HR: classify the interrupts
ME: TRAP
RST 5.5, 6.5,7.5
INTR.

HR: What is an Interrupt:
ME: An Interrupt is an signal send by external device to processor, to performing a specific task, the current execution is stopped.

GENERAL HR ROUND

HR: Tell about yourself
ME: I'm Vigneshwaran came form Madurai. I completed my higher studies in **"Thiagarajar Model Hr. sec School"**…....

HR: Who is your role model?
ME: My father is my role model. He is very kindest man,…….

HR: What's your positive?
ME: very punctual, honest good listener…….

HR: Tell about Aricent
ME: Aricent is software as well as product based company. There are 12,000 employees. There are several fields such as networking, embedded fields, semiconductors and digital design process etc

HR: Why are you choose the Engg
ME: After completing the +2, I'm interested in the physics and maths.

HR: **why i should select you for our company**
ME: I'm punctual, honest, and regularity. I'll make your profit more than you think.

HR: **Thank you Vigneshwaran.**
ME: Thank you sir.

ARICENT INTERVIEW

I am PRADEEP KUMAR. Here I tell about the overall experience in the Aricent interview

COMPUTER BASED TEST:-

The first round of every interview is aptitude based test. For cracking the questions with accurate answers shortcuts of all the questions are necessary. It also matters with keep an eye on the ticking clock on the corner of screen but if we know the shortcuts it will be very easy to solve in time.

Most of the questions asked were from the Quantitative aptitude, numbers and a few Logical questions Train and distance Problems, Time and distance Problems ,Graphs and charts ,Numbers, Simple Interest, profit and loss ,Logical questions ,Data structure Programming ,Basic Programming in C and C++ ,K-Map, SOP and POS problems ,Concepts of Filters As I have mentioned earlier shortcut method for every questions is important. There is an alternative option to solve the problems, by choosing the choices relevant to the questions. Try to solve the coding on what way you know about it. There must be some knowledge require to answer digital electronics section. All the questions were from the basics. Study well the concepts is the only thing that helps to solve the questions from digital side.

TECHNICAL HR:-

In technical HR round, we need to prove ourselves that we are good in programming and the basic concepts of the oops like Inheritance, Polymorphism, Abstraction and Encapsulation. I Present here the conversation format of interview

CANDIDATE : Good morning sir.

INTERVIEWER : Good Morning, Please Sit down.

CANDIDATE : This is my Profile sir.

INTERVIEWER : Yeah ok, In which platform you did your mini projects?

CANDIDATE : I had done all my projects in the arduino and Raspberry pi processors.

INTERVIEWER : In your projects what kind of role do you take part?

CANDIDATE : Sir, I am responsible for coding and project components management.

INTERVIEWER : Can you write the Raspberry pi coding in this paper?

CANDIDATE : Yes sir, Here is this.

INTERVIEWER : Can you explain this program and how is this works in your projects?

CANDIDATE : Yes sir. (Explain the project coding)

INTERVIEWER : Have you know about Polymorphism and Inheritance?

CANDIDATE : Yes sir, **Polymorphism** is the ability of an object to take on many forms.

INTERVIEWER : Write a program to describe polymorphism?

CANDIDATE : (Write the program and explain the program)

INTERVIEWER : Do you know anything about digital electronics?

CANDIDATE : I know about flipflop and multiplexers.

INTERVIEWER: Explain T flipflop? Don't dig too much Just explain and draw.

CANDIDATE : (Explain the flipflop concept and draw the diagram)

INTERVIEWER: Explain about MOSFET and SMPS?

CANDIDATE : (Explanation and the block diagram of SMPS)

INTERVIEWER: In C, What is your well known program?

CANDIDATE : Palindrome number and sorting.

INTERVIEWER: Write any one of the program?

CANDIDATE : (Writing the program and explain the program)

INTERVIEWER: Ok this enough, lets end this session. Do you have any questions?

CANDIDATE : Yes sir I want to know about the Neural Networking that I read in your website

INTERVIEWER: (He explains about the Neural Networks), anything else?

CANDIDATE : Thank you sir. No sir that's all.

INTERVIEWER: OK Pradeep, We have good session. Thank you.

CANDIDATE : Thank you sir.

GENERAL HR:-

This is much easier than the technical session, all the things asked is about your personal information, Management proficiency, Team management and about communication. They also view the confidence level of your personality. Here you can tell about your own interest towards the technology and your personal things. Always start the conversation first by wishing the interviewer.

CANDIDATE : Good morning sir.

INTERVIEWER: Good morning, sit down please,Had your breakfast?

CANDIDATE : Yes sir

INTERVIEWER: Tell me about your personality?

CANDIDATE : (Tell about yourself, and about the attitude towards seeing any problem)

INTERVIEWER: Can you give a speech for 3 mins about any technical thing that you had read?

CANDIDATE : Sir I can, I would like speak about Tesla cars. (Continued for 5 minutes)

INTERVIEWER: Have you been worked as team? And What is your part in it?

CANDIDATE : Sir In my project.....

INTERVIEWER: Tell me any kind of tough situation that you have handled in non-technically

CANDIDATE : Once upon a time when we played cricket.....

INTERVIEWER: Ok thank you for this conversation.

CANDIDATE : Thank you.

CONCLUSION:-

Don't afraid about anything just speak formally infront of them. They just need to analyze whether you are capable of work under tough situation. Make yourself more comfortable while answering. Finally I would like to thank all the teachers and my friends for giving me great experience to handle the interview.

NAME: **SWATHIKA**

YEAR : **IV yr-'C'**

ROLL NO: **142014**

I am here to share about my first interview experience in ARICENT company.

FIRST ROUND:

PART A (80 mins): It includes verbal, quant's, logical and reasoning.
: All are MCQs type
: No negative marks**.** : We can skip the questions (My experience is to concentrate in quant's because logical and reasoning will be easy to solve but quants need a regular practice)

PART B (80 mins): Programming 2 questions-(**30 mins**)-oops concepts.
:Data structures 1 question-(**15 mins**)
:Digital design 1 question-(**15 mins**)-Designing type questions will be there.
:Microprocessor 1 question-(**15 mins**)-Concentrate in programmings.
: We **can't** skip the questions. Corresponding questions should be solved in the corresponding time. (Time has been allotted separately so utilize the whole time and read out fully the questions and answer it in oops concept like **c** or **c++** or **java**)

TECHNICAL ROUND

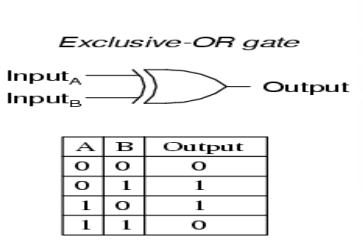
Me :Good evening sir.

Technical HR:Good evening. Do u have energy to continue this round?
Me : Yes sir.

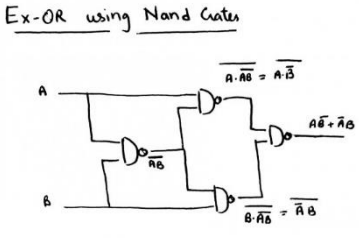
Technical HR: By seeing your mark sheets you were interested in science have you Achieved anything in science based competitions?
Me : yes sir I had won **YOUNGER SCIENTIST AWARD** in the **18 th National children science congress (2010).**

Technical HR: say me about that something?
Me : it is project report on the **topic: An analysis on microorganisms Which make the soil fertile? It is about the differentiation between Natural fertilizer and artificial fertilizer……..**

Technical HR: ok, let's go for technical. Design the XOR GATE using NAND gate and Draw the truth table and symbol for XOR gate so that you Can design it easily.
Me : ans written in paper

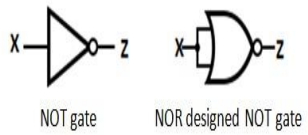


A	B	Output
0	0	0
0	1	1
1	0	1
1	1	0



Technical Hr: Design NOT gate using NOR gate:

Me :



NOT gate NOR designed NOT gate

Technical Hr: write any 8085 microprocessor program that should not extend beyond 5-7 lines And explain it.

Me : iam explained a simple addition program

Technical Hr: what is the threshold voltage value for forward biased diode and reverse biased? Diode?

Me : 0.3 for forward biased & 0.7 for reverse biased

Technical Hr: write a c program to find an area and that area should be divisible by 4 and explain it

Me : I explained a program

ROUND 3: GENERAL HR

Me: Good morning sir

Hr: Good morning, introduce yourself.

Hr: Have u met any large crowd and given any presentation?

Me: Yes sir, I had met a large crowd and given presentation and achieved an award younger scientist in my school days.

Hr: you are buying one product for rps10, 000 and you are the sales man and your management has told you to sell that product for 10,000 and should not sell that under 10,000 you will be dismissed from the job they were warning you but you have a arrogant buyer he asks your product only for 8000 how do you handle the both?

Me: sir my answer is I will try to console my buyer to buy that product for 10,000 by explaining the product clearly to him by saying the advantages of this product and in what way it would help him. but if he was arrogant I would him for 8000 and inform my management that sir we know that it was best product and we only go for the best if I sells him to 8000 or 10000 it should be the best so that he tells to the friends of hi to the public about the best of the product so that there will be a increase in customer so that during that time we will have a demand for that product and we can increase a cost to about 12,000 at that time so we can gain the money for this product

Hr: Have you gone through our website searched for anything?

Me: Yes sir, I had a doubt about your hiring, that only this year you were hiring Non-IT dept so I went through year of innovative things and recently your company has started new technologies in hardware so I read about that.

Hr: ok swathika, why should I hire you?

Me: sir your company is product Development Company for telecommunication based on innovative thoughts and I have some innovative thoughts and ability to design and develop and implement the product and can give solutions to the customer on the product which I had developed.

Hr: ok thank you swathika, be positive, and wait for the results.

Me: thank you sir

M.SATHISHKUMAR

KLNCE,EEE

MY EXPERIENCE ON ARICENT CAMPUS RECRUITMENT:::

It consists of 3 rounds 1)online test on aptitude ,logical, coding , microprocessor and DLC

2)Technical HR

3)Direct HR

ONLINE TEST:

- More of the aptitudes on time and work, ratio,coding decoding, time speed and distance,percentages.
- Coding section consists 3 tough programms each program having long story and many inputs. Eg:The logic is save the 10 cows name count its milk rate repeatedly, find the given series in progamme like. And arrangements of lines in program like jumbled sentence.
- The DLC having the truthtable forming from question from the truthtable find the K-map groups from that drive an expression.
- The microprocessor section having a simple MP program from that 30 questions asked that like the value of accumulator or any register in line 12,5,24.

TECHNICAL HR:

HR: tell yourself
ME: iam telling my achievements like project winning, 100% attendance, my details,my marks, iam missed the long term,short term goal,strengths, weaknesses.

HR: what is your area of interest
ME: sir. Iam interested in core subjects iam listed it machines, power systems,power electronics .....and basics of oops concepts and C.

HR: what is stepper motor explain me to understand
ME: i explained the operation excitation steps, advantages, and applications of stepper motor correctly.

HR: tell me about your prize won project
ME: i explained idea of project advantage and my part in our team project

HR: good . write a simple program on pointers , call by value, call by reference and explain the concepts

ME: (he give nearly 15 minutes at the time iam writing the program and he writing the paragraph about me in a file.) after i finished i explained pointer uses, declaration, and program of call by value and iam explained call by reference concept wrongly.

HR: draw an architecture of 8085 MP and explain each blocks
ME: iam drawing some blocks in 8085 and explained in single word.

HR: why we store the datas in registers instead of RAM memory
ME: i can't answer the question then he explained the answer to me.
(***** the HR focused in **how we studied our core concepts(understanding level), some knowledge in micromprocessor, strong in basic concepts of codings, ideas in electronics, attitude**)

DIRECT HR:

HR: samely he asking. Tell me yourself
ME: iam explained my self. (He tell don't move your hands.)

HR: what are the special skills of you compare than others. tell only your mind tells
ME: leadership, encouraging others, developing the ideas, analyzing the my interest things.

HR: what is your best and worst day in your college
ME: my best day is firstday (i tell my first day experience) and i have a lot of worstdays

HR: (he gave me some situations like iam a marketing seller if i have lost day to sell more products what will i do and if iam failed that task what will do with my manager)

HR: (he got my communication is poor) you think this communication is okay with others
ME: it's not enough sir i will surely make the best at the end of year (then he gives lot of advices to speak fluently in english)

HR: did you seen our website
ME: no sir but, i have seen your presentation of aricent at morning

HR: (he have a seet of results of the online round of mine) how you do the morning test
ME: aptitude is good,coding is ok,MP and DLC is good. I think so sir

HR: why i should select you for our company
ME: sir iam punctual , honest, and regularity. I'll make your profit more than you think
(***** the HR conversation is more than 30 minutes. The final HR is focused in **attitude, confident level,communication, the technical HR notes of ours**)

******* Thank you ********

NAME : G.YUVANA SHREE MURUGA PRIYA,

ROLL NO : 142310,

YEAR AND SEC : IVYEAR/ C Sec.

ROUND 1:

PART A(80 mins): includes quants ,verbal, logical questions**.**

-all are multiple choice questions

-we can skip questions.

PART B (80 mins): Programming 2 questions-(**30 mins**)-C,C++ OR JAVA.

-Data structures 1 question-(**15 mins**)

-Digital design 1 question-(**15 mins**)-Designingtype

-Microprocessor 1 question-(**15 mins**)

-We **can't** skip the questions.

ROUND 2:TECHNICAL HR:

ME:Good morning sir!

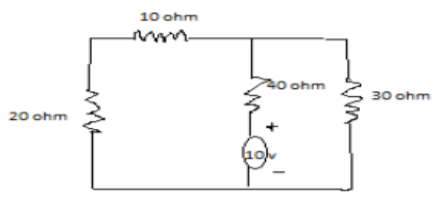
HR:Good morning!Have your seat.Where are you from?
ME:I'm from Madurai .

HR:Where did you do your schooling?
ME:I did my 10^{th} in SEV School Madurai and I did my 12^{th} in SRV School Trichy.

HR:What is your Area of interest?
ME:Power plant engineering and Design of electrical machines.

HR:What is the difference between the motor and generator?
ME:MOTOR:Electrical input and mechanical output.
GENERATOR:Mechanical input and electrical output.

HR:Solve the circuit and find equivalent current and power.



ME:20 ohm and 10 ohm are in series.
=20+10=30 ohm
*30 ohm and 30 ohm are in parallel: 1/R=1/30+1/30=>R=15ohm.
*40 ohm and 15 ohm are in series=40+15=>R=55 ohm
*EQUIVALENT CURRENT:
V=IR
I=V/R=10/55=>I=0.18A
*POWER:
P=VI=10*0.18=>P=1.8W

HR:Write a program to find the length of the string.
ME:I explained the program.

HR:Which processor do you know well?
ME:8085 Processor.

HR:Write a program to add two numbers in 8085 processor.
ME:

```
LXI H 4000H : "HL points 4000H"
MOV A, M    : "Get first operand"
INX H       : "HL points 4001H"
ADD M       : "Add second operand"
INX H       : "HL points 4002H"
MOV M, A    : "Store result at 4002H"
HLT         : "Terminate program execution"
```

ROUND 3:GENERAL HR:

ME:Good evening sir!

HR:Good evening!Please be seated!Tell about yourself.
ME:I'm G.Yuvana shree muruga priya ,from Madurai.I have one younger brother studying 12^{th} standard.My mother is a home maker.....

HR:What is your preference in job location Chennai or Bangalore?
ME:I'm comfortable with both the places.

HR:Do you know about our company rules, regulations and bond details.
ME:Yes sir!

HR:Okay!The result will be announced around 6:30 pm.
ME:Thank you sir!

MY FIRST INTERVIEW EXPERIENCE

Hi friends,

I am **Jananie, EEE –A final year**, KLNCE. I am here to share my first interview experience.

ROUND 1 – ONLINE TEST

We had online test for nearly **150 minutes**. No negative marks for wrong answers.

Round 1 consists of two parts,

1. Part A – Aptitude and general MCQs from Microprocessor and Digital systems.

   This part was for 80 minutes. There was no time bound among various sections. The sections included were,

   - **Quantitative aptitude**
   - **Logical and reasoning**
   - **Basic MCQs on Microprocessor and Digital systems**

2. Part B – Coding and Code analysis. This existed for 70 minutes. This section had time bound.

   - Re-arrange the jumbled program (in C or C++), Coding (any language) – 20 minutes
   - Coding in Data structure – 15 minutes
   - Logic diagram and K map for the given question – 15 minutes
   - Code analysis (Microprocessor) – 20 minutes

My preparation for Round 1:

The topics I prepared for Round 1 were,

- Aptitude questions from www.indiabix.com**(time and work, ratio & proportion, profit & loss)**
- Basic logics for coding from my notes **(Fibonacci series, leap year, Armstrong, palindrome, prime number, reverse a number and so on …)**
- Basic topics such as **Arrays, Strings, Pointers, Inheritance, Polymorphism, looping, conditional statements** etc.
- Instruction set of 8085 Microprocessor

ROUND 2 – TECHNICAL INTERVIEW (on 04.08.2017)

Myself: Good afternoon Sir.

Interviewer: Good afternoon. Have your seat.

Myself: Thank you, Sir.

Interviewer: Give an outline about your profile.

Myself: I am Jananie from Madurai. I completed my schooling in S.B.O.A……

Interviewer: OK. Tell me about your co-curricular activities.

Myself: I had done four mini projects at my college and in that I have won 2nd Prize for "Smart Card Based Unmanned Petrol Bunk" which I did in 6th semester….

Interviewer: Which language are you comfortable with?

Myself: C and C++ Sir.

Interviewer: Explain structures in C with an example program.

Myself: Structure is collection of different data types grouped together and each element in structure is called its member. Example,

Interviewer: Why should we go for C++ instead of C?

Myself: C++ has OOPS concepts such as inheritance, polymorphism, data encapsulation, data abstraction, class, objects etc. Data is more secured in C++ than in C.

(After seeing my area of interest)

Interviewer: What do you mean by network theorems?

Myself: Network theorems are theorems used for analysis of electric circuits such as KCL, KVL, thevenin's theorem etc.

Interviewer: What do you mean by KCL?

Myself: KCL means Kirchhoff's current law. It states that "Current entering the node is equal to current the node."

Interviewer: Write any program using 8085 microprocessor instructions.

Myself:I explained the program…

Interviewer: Good. Thank you Jananie.

Myself: Thank you Sir.

ROUND 3 – HR INTERVIEW (on 04.08.2017)

Myself: Good evening Sir.

HR: It's Good afternoon Jananie.

Myself: Sorry Sir, Good afternoon.

HR: Give me a short profile about you Jananie.

(Since he mentioned my name whenever he asked me question, I didn't mention my name in the self-intro.)

Myself: I'm from Madurai. I completed my schooling in S.B.O.A…

HR: Your academics is good Jananie. But why you had gone down in college? You have secured above 90% in 10^{th} and 12^{th} but why you were not able to get above 9 CGPA? (My CGPA is 8.53)

Myself: I tried my level best in all the examinations Sir. But somehow I get the same result. I'm learning more and more new strategies every time to improve my scores.

HR: 8.53 is not a bad CGPA Jananie. It's too good but if it is also above 90%, all of your academics will be above 90%. Ok, why did you choose EEE?

Myself: I'm very fond of Mathematics. Since most of EEE subjects are close to mathematics, I chose EEE.

HR: Explain me about anyone of the projects you have done.

(I explained about "Smart Card Based Unmanned Petrol Bunk", the project I did in 6^{th} semester. I told the technology used, description, operation and soceital importance of it.)

HR: You have completed OCJP. So your are interested in software I think. Who suggested you to learn these languages?

Myself: My department Professors suggested Sir.

HR: Have you taken any seminars or paper presentation outside the college?

Myself: No Sir.

HR: Ok Jananie. Now I will you a situation. You must put yourself in that situation and tell me what decision will you make.

Myself: I will implement the product in the management itself and make it benificial for our purpose.

HR: Ok Jananie, your communication and attitude is so good, You have also performed brilliantly in test and technical round. The only thing I suggest you is to improve your self-confidence. Try to overcome the fear befor you join in Aricent family. Thank you.

Myself: Sure Sir, thank you.

NAME : R.GAYATHRI.

ROLL NO: 142309.

YEAR&SEC: IV YEAR A SEC

PREPARATION:

- First, I prepared for aptitude. HINTS: Don't prepare all the topics and get confused. Go through some important topics that are frequently asked and easy for you. Because they are fixing cut off, so that's enough. Be strong in your selected areas.
- Then important thing is your resume. Your resume should be professional and neat. You can able to answer each and every word of your resume. Because most of the questions are from your resume especially from the area of interest. Mention a selected area (Eg : synchronous motor).
- In programming area focus on one language in what you are strong. If you are strong in more than one language it's your extra benefit.
- Try to attend some mock interviews so that you can easily face the HR round without tension.

ROUND 1: (ONLINE TEST)

- Keep your mind relax without any tension.
- Attend the questions after reading it completely. Better read once clearly without seeing the options. Try to solve it and look at the answer.
- Skip the questions if you are not able to answer and go for the familiar areas so that you will get confidence.
- In programming questions try your best. Don't skip any programs since company is preferring this than aptitude.
- Try to solve placement papers available on net. Time management is also very important.
- Questions asked for me,
- Aptitude: Time and work
  Ages
  Profit and loss
  Train
  Percentage
  Verbal
  Logical reasoning.

ROUND 2: (TECHNICAL HR)

- Try to keep eye to eye contact with HR, even if you are not confident with your answer.
- Answer confidently with what you know and try to satisfy them with your answer.
- Go through all concepts included in your area of interest especially basic concepts.
- In my interview, the topics asked for me are digital electronics, machines , microprocessor, power electronics and finally programming.
- Try to show you positively all the times. Never show your weakness to them. If you are unable to answer them try your maximum level in that particular area. This shows your potential.
- Most of the questions are from your answers so answer carefully.

Conversation in Technical HR:

HR: How to implement EX-OR gate without using OR gate?
ME: I implemented it using AND & NOR, NOT gate.
HR: Can you do direct division in 8085 microprocessor?
ME: No sir, repetitive subtraction is only possible in 8085.Then he asked me to write the program.
HR: Tell me about synchronous motor and for what purpose we are using it?
ME: Synchronous motor is not self starting motor operating at synchronous speed. It can be operated at lagging, leading and unity power factor. It can be used for power factor correction.
HR: Write a program to check for even number for N numbers.
ME: I wrote a C program using "if" and "for" conditions.
HR: What is the breakdown value of thyristors?
ME: Its 0.7
HR: Switches used for fans
ME:TRIAC and its a bidirectional, three terminal semiconductor device used in swiching operation.

ROUND 3: (HR)

Attitude is the most important thing observed in HR round. Communicate fluenty and speak confidently.

HR: Tell me about yourself

Family background

Hobbies

Why do you prefer IT?

After two years what will be your aspiration?

What are the present technologies going on?

BE POSITIVE

ALL THE BEST


VISION
To become a high standard of
Excellence in Education,Training
And Research in the field of
Electrical &Electronics Engineering
And allied areas
time to
go green
MISSION
To produce excellent,innovative
And Nationalistic Engineers
With Ethical Values and to
Advance in the field of Electrical
&Electronics Engineering and
Allied areas