

## K.L.N.COLLEGE OF ENGINEERING

### THREE DAY FDP ON

### Design and Testing of Electronic Circuits

#### REGISTRATION FORM

09<sup>th</sup>, 10<sup>th</sup> & 11<sup>th</sup> June 2016

Name:

Academic Qualification:

Designation:

Faculty:

Experience:

Institution Address:

Communication Address:

Mobile:

E-mail:

Accommodation required: Yes / No

DD No.:

Dated:

Amount:

Date:

Signature

#### Venue date & time:

The workshop will be held at EIE department / KLNCE Campus from 09-06-16 to 11-06-16 between 9.00 A.M. & 3.30 P.M.

#### How to apply:

Xerox copy of the application duly signed and recommended by the Head of the Institution is to be sent to the coordinator along with Demand Draft on or before 04.06.2016.

**Eligibility:** Faculty from Engineering colleges, Polytechnic colleges, having Degree in ECE / EIE / EEE / ICE can participate.

#### Registration Fee:

Faculty : Rs. 700/-

Student : Rs 300/-

Demand Draft drawn in favor of **“The Principal, K.L.N College of Engineering” payable at Madurai** should be sent along with the registration form. No TA/DA will be provided for the participants. Accommodation will be provided at KLNCE campus depends on the availability at nominal charges. Participants should send the scanned copy of filled registration form to [eie@klnce.edu](mailto:eie@klnce.edu) on or before 04.06.2016.

## A Three Day Faculty Development Program on “Design and Testing of Electronic Circuits”

09<sup>th</sup>, 10<sup>th</sup> & 11<sup>th</sup> June 2016

Organized by



Department of Electronics &  
Instrumentation Engineering

**K.L.N. COLLEGE OF ENGINEERING**  
(An ISO 9001:2008 Certified Institution)  
Pottapalayam-630 612, Sivagangai Dist  
( 11 km From Madurai City)

Ph: 0452 - 2090971

URL: [www.klnce.edu](http://www.klnce.edu)

**Convener**

**Dr.A.V.Ramprasad, Principal**

**Co ordinators**

**Prof. S. Nagammai, HOD/EIE**

**Dr.A.Umarani, Prof/EIE**

**Co - Coordinators**

**N.Vishnu Devi,AP/EIE**

**R.Naveeth Kumar,AP/EIE**

### Institution Profile:

K.L.N College of Engineering is the first self financing Co-educational Engineering Institution in Madurai incepted in 1994 by munificence of Philanthropist & well wishers in Sourashtra community which is a Linguistic minority in Tamilnadu. Our college is sponsored by a committee of eminent Industrialist & Academicians lead by enthusiastic, and Industrialist Amaranar Shri. K.L.N. Krishnan. Our college has been approved by All India Council for Technical Education, New Delhi, and it is affiliated to Anna University, Chennai. The college is located in the south eastern outskirts of Madurai and is 11 kms away from Madurai City. The college runs Seven Undergraduate engineering programs and Seven Postgraduate programs. The College has excellent infrastructure with College Automation and 132 Mbps Internet bandwidth.

### Department Profile:

The department of Electronics & Instrumentation Engineering was established in the academic year 2001-2002, with an intake of 60 students and faculty strength of 12 with three Professors & nine well qualified faculties. The department has dedicated faculty members with different field of specialization. The laboratory is well equipped with all latest instruments to cater to the need of the syllabus and it is altogether worth about 60 lakhs. It is a pride to our department to mention that every year our students are bagging at least 5 university ranks. The department has signed MoU with Yokogawa India limited, i max Industrial Automation Technology & National instruments which organizes in house training program such

as DCS, SCADA, PLC, LabVIEW etc., so as to provide industrial exposure. Our students are placed in reputed companies like Honeywell Technologies, Infosys, Wipro, CTS, TCS, etc. in good number consistently.

### Scope of the Program:

The objective of this course is to introduce electronic circuits and its analysis to the participant and to impart knowledge on solving simple Electronic circuits. The programme also focuses upon grooming the young academicians into effective teaching and exploring the concepts using National Instruments MYDAQ Card. This faculty development program is devoted mainly to the fundamental theory addressing the related theoretical and practical aspects on Electronic Circuits.

### Course Contents:

- ❖ Transistor, FET & MOSFET biasing
- ❖ Simple Amplifier circuits using BJT, FET, MOSFET
- ❖ Analysis of small signal low & high frequency model of BJT & FET.
- ❖ Design and Analysis of large signal amplifiers
- ❖ Design and analysis feedback amplifiers.
- ❖ Design LC and RC oscillators, wave shaping circuits, multivibrators, blocking oscillators and time base generators.
- ❖ Performance analysis of tuned amplifiers.
- ❖ **Experimental verification of designed circuits.**
- ❖ **Discussion on GATE questions**

### Resource Persons:

**Experienced faculty members from reputed institution and from our own institute will handle sessions.**

## Recommendation from Head of the Institution

This is to certify that Mr./Mrs/Ms.-----  
-----is sponsored to participate in the Three Day FDP on “**Design and Testing of Electronic Circuits**” on 09-06-2016 to 11-06-2016 at K.L.N. College of Engineering Pottapalayam, Sivagangai District.

Date:

Signature & Seal  
Principal/HOD

### Coordinators

**Prof. S. Nagammai, HOD/EIE**

**Dr.A.Umarani, Prof/EIE**

### Contact Details:

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