

K.L.N. COLLEGE OF ENGINEERING, POTTAPALAYAM

DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING

FACULTY DEVELOPMENT TRAINING PROGRAMME ON

EC6202 - ELECTRONIC DEVICES AND CIRCUITS

19.06.17 to 25.06.17

SCHEDULE OF FACULTY DEVELOPMENT TRAINING PROGRAMME

S.No	Day	Time	Topic/Activity	Resource Person
1.	19.06.2017 (Monday)	09.00 am-09.30 am	Registration	
		9.30 am -10.45 pm	PN junction diode -structure, operation and V-I characteristics, diffusion and transient capacitance - Rectifiers - Half Wave and Full Wave Rectifier	Dr.A.Umarani, Prof/EIE/KLNCE
		11.00 am-12.30 pm	Rectifiers - Half Wave and Full Wave Rectifier	Dr.A.Umarani, Prof/EIE/KLNCE
		01.30 pm-03.00 pm	Display devices- LED, Laser diodes-Zener diode characteristics- Zener Reverse characteristics - Zener as regulator	Dr.S.Myhili, ASP/ECE/PSNA
		03.00 pm-04.45 pm	Practical Session	Mrs.R.Thenmozhi AP/EIE/KLNCE Mrs.K.K.Sharmila Mrs.N.K.Shanthi
2.	20.06.2017 (Tuesday)	9.30 am-10.45 pm	BJT, JFET, UJT, Thyristor and IGBT - Structure and characteristics	Dr.Prince, ASP/EEE/KCET
		11.00 am-12.30 pm	BJT, JFET-Biasing	Prof.S.Nagammai, HOD/EIE/KLNCE
		01.30 pm-03.00 pm	MOSFET- structure, operation, characteristics and Biasing	Mrs.N.Vishnu Devi AP/EIE/KLNCE
		03.00 pm-04.45 pm	Practical Session	Mrs.N.Vishnu Devi AP/EIE/KLNCE Mrs.N.K.Shanthi Mrs.V.Karthi
3.	21.06.2017 (Wednesday)	9.30 am-10.45 pm	Basics of devices and analysis- Industrial Perspective	Mr.G.Soundra Pandiyan Project Lead, Data Patterns, Chennai
		11.00 am-12.30 pm	Basics of analog circuit analysis- Industrial Perspective	
		01.30 pm-03.00 pm	BJT small signal model	Prof.S.Nagammai, HOD/EIE/KLNCE
		03.00 pm-04.45 pm	Basics of analog circuit analysis- Industrial Perspective	Mr.G.Soundra Pandiyan Project Lead, Data Patterns, Chennai
4.	22.06.2017 (Thursday)	9.30 am-10.45 pm	Analysis of CE, CB, CC amplifiers	Prof.S.Nagammai, HOD/EIE/KLNCE
		11.00 am-12.30 pm	Analysis of FET amplifier - Gain and frequency response	

		01.30 pm-03.00 pm	High frequency analysis	Prof.S.Nagammai, HOD/EIE/KLNCE
		03.00 pm-04.45 pm	Practical Session	Mrs.R.Thenmozhi AP/EIE/KLNCE Mrs.N.K.Shanthi Mrs.V.Karthi
5.	23.06.2017 (Friday)	9.30 am-10.45 pm	Differential amplifier - Common mode and Difference mode analysis	Prof.S.Nagammai, HOD/EIE/KLNCE
		11.00 am-12.30 pm	FET Differential amplifier	Prof.S.Nagammai, HOD/EIE/KLNCE
		01.30 pm-03.00 pm	Power amplifiers -Types (Qualitative analysis)	Mr.S.Rajalingam, Asst.Prof/EEE/KLNCE
		03.00 pm-04.45 pm	Neutralization methods	Dr.A.Umarani, Prof/EIE/KLNCE
6.	24.06.2017 (Saturday)	9.30 am-10.45 pm	MOSFET small signal model BIMOS cascade amplifier, Single tuned amplifiers - Gain and frequency	Dr.N.B.Balamurugan, Asso.Prof/ECE/TCE
		11.00 am-12.30 pm	Feedback amplifiers - voltage / current, series, Shunt feedback -positive feedback	Mr.S.Jeyamurugan, Asst.Prof/EEE/KLNCE
		01.30 pm-03.00 pm	Condition for oscillations, phase shift - Wien bridge, Hartley, Colpitts and Crystal oscillators	Dr.D.Pradeep Kannan Prof/EIE/KLNCE
		03.00 pm-04.45 pm	Practical session	Mrs.N.Vishnu Devi AP/EIE/KLNCE Mrs.N.K.Shanthi Mrs.V.Karthi
7.	25.06.2017 (Sunday)	9.30 am-10.45 pm	Discussion on Anna University Solved Question	Prof.S.Nagammai, HOD/EIE/KLNCE
		11.00 am-12.30 pm		
		01.30 pm-03.00 pm	Discussion on GATE Question	Prof.S.Nagammai HOD/EIE/KLNCE
		03.00 pm-04.45 pm	Valedictory function	

10.45 to 11.00 am- Tea break

12.30 pm-1.30 pm Lunch