

## ABOUT THE INSTITUTION

K.L.N. College of Engineering is the first self-financing Co-educational Engineering College in Madurai started in the year 1994 and is sponsored by a committee of eminent industrialists and academicians. This college has been approved by AICTE, New Delhi and affiliated to Anna University. This college is an ISO 9001:2015 certified institution and offers seven U.G. programmes and five P.G. programmes. This college campus is located in a quiet environment about 11 KM from Madurai city. This college has excellent infra-structure, well-equipped laboratories, library with online subscriptions and other general facilities.



### Convener

Dr. A.V.Ramprasad,  
Principal, K.L.N. College of Engineering

### Co-Convener

Dr. S.M.Kannan, Professor & Head  
Department of EEE,  
K.L.N. College of Engineering

### Coordinator

Dr.K.Gnanambal,  
Professor. Department of EEE,  
K.L.N. College of Engineering

### Co-Coordiators

Ms.E.Jeyasri, AP/EEE  
Ms.R.Divya, AP/EEE,  
K.L.N. College of Engineering

### Contact Details

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TWO DAYS

FACULTY DEVELOPMENT PROGRAMME

on

“Linear Integrated Circuits”

15<sup>th</sup> & 16<sup>th</sup> June 2017



**Estd:1994**

*Organized by*

Department of

**Electrical and Electronics Engineering**

*(Accredited by NBA up to 30.06.2019)*

**K.L.N. College of Engineering**

*(An ISO 9001:2015 Certified Institution)*

*(Approved by AICTE, New Delhi &*

*Permanently Affiliated to Anna University, Chennai)*

**Pottapalayam – 630 612**

**Sivagangai District, Tamil Nadu.**

**(11Km from Madurai City)**

www.klnce.edu

## ABOUT THE DEPARTMENT

The Department of Electrical and Electronics Engineering was started in the year 1994 with an intake of 40 and with the latest intake of 120 in 2011. Started offering M.E. in Power Systems Engineering in the year 2004 with an intake of 18 and intake increased to 24 in 2012. Accredited four times (2004, 2008, 2012 and 2016) by the National Board of Accreditation, New Delhi, and recognized as Research Center of Anna University, Chennai. The department is doing consultancy in “Industrial Harmonic Audit” and “Energy Audit” for the benefit of Public sector undertaking and Private companies. The department is fully equipped with the latest equipments in Electrical Machines Lab, Power Electronics Lab, Control, Measurements and Instrumentation Lab, Electronics, Digital Circuits and Microprocessor lab and has excellent computing facilities with latest simulation softwares in Power system Simulation Laboratory. Texas Instruments based laboratory is exclusively set up to give hands on training to the students and researcher on the advanced topics Electronics and Integrated Circuits.

## TARGET AUDIENCE

Faculty members and PG students from EEE, ECE and Instrumentation Branches.

## ABOUT THE FDP

This FDP paves a platform for the faculty members and PG students to improve their teaching skill on the course “Linear Integrated Circuits”. The objectives are:

- To impart knowledge on the subject “Linear Integrated Circuits”.
- To enrich the knowledge in simulation of circuits using open source software.
- To provide hands on training on Linear IC Applications.

## REGISTRATION FEE

**Registration Fee per participant: Rs. 500/-.** The fee includes the course material, tea and lunch. Participants, who would like to commute daily from Madurai, can avail the bus transport of K.L.N.C.E. The fee should be sent by **Demand Draft** drawn in favour of “**The Principal, K. L. N. College of Engineering**” payable at Madurai. The fee does not cover accommodation and no TA/DA will be provided to the participants. However, on request and depending on the availability, accommodation could be provided in the student hostel at moderate charges.

Last Date for Registration: 8<sup>th</sup> June 2017.

## Two days Faculty Development Program on Linear Integrated Circuits

### REGISTRATION FORM

1. Name :
2. Designation :
3. Department :
4. Institution & Address for  
Communication :
5. Phone/ Mobile No. :
6. E-mail address :
7. Accommodation required : Yes/ No
8. Demand Draft particulars :

Date:

Signature

### SPONSORSHIP FROM THE HEAD OF THE INSTITUTION / DEPARTMENT

This is to certify that Mr/Ms \_\_\_\_\_ of \_\_\_\_\_

\_\_\_\_\_ is sponsored/permited to participate in the FDP on “Linear Integrated Circuits” on 15<sup>th</sup> & 16<sup>th</sup> June 2017 at K.L.N. College of Engineering, Pottapalayam, Sivagangai Dist.

Date:

Signature  
(Designation and Seal)