

## About the Institute

K.L.N. College of Engineering has been the first self-financing co-educational Engineering College started in Madurai in 1994. The College has been affiliated to Anna University and approved by All India Council for Technical Education (AICTE). The College is located in the south eastern outskirts of Madurai and is 11Km away from Madurai city. The college runs 7 undergraduate engineering programs and 5 Master Programs including M.E. Communication Systems.

## Department of ECE

The Department of ECE was started in 1994. It has an intake of 120 students. The Department has adequate infrastructure with spacious classrooms, conference halls and well developed eight laboratories having the advanced designing tools like MATLAB, Orcad P-Spice, Xilinx, IE3D and modernized equipments like spectrum analyzer, microwave power meter, GPS kit. The core members of the faculty have rich academic experience and wide industrial and R&D exposure and are well suited for minimizing the gap between academy and industry. The Department initiates the U.G., P.G. & research projects in Embedded Systems through the Centre for Embedded Systems which is based on the collaboration of institution & Embedded industry. The Department library has over 1500 books and magazines and journals. AICTE, New Delhi has sanctioned fund as under MODROB scheme for modernizing Microwave Lab of this department.

## Madurai

Madurai is a place of great historical and cultural importance. It was the capital of Pandya Kingdom. Madurai is famous for its temples with its rich architecture and sculptural works such as Meenakshi Temple and it is also famous for textiles, handlooms and silk sarees. Madurai is the second largest city in Tamil Nadu with many Educational Institutions and world-class hospitals. It is well connected by air, rail and road.

## Objectives of the Workshop

To learn & to create awareness about,

### Theory Session:-

- Introduction to VLSI Design Flow – Front-end & Back-end Design with Xilinx
- Industrial expectation on VLSI Design Engineer
- Basics of FPGA & Details about Xilinx Products & Architecture overview

### Demonstration Session:-

- Xilinx:- ISE Tool for Simulator & Synthesis
- Xilinx:- Embedded Development Kit for Embedded applications in VLSI platform
- Xilinx:- Chip Scope Pro for Signal analysis
- Xilinx:- Plan ahead for Floorplanning & Partitioning in FPGA
- Xilinx:- System Generator for DSP applications

One-Day Workshop  
on

**“CONCEPTS ON VLSI  
WITH XILINX PRODUCTS”**

10-07-2010  
(Saturday)

**REGISTRATION FORM**

Name :

Department :

Year :

Section :

Organization :

Mobile No. :

E-mail id :

Date: \_\_\_\_\_ Signature of the Applicant

Signature & Seal of the H.O.D.:

## Who can Register?

The programme is open for all engineering students. Interested Research Scholars & P.G. students are also allowed to register. Application can be downloaded from the website [www.klnce.edu](http://www.klnce.edu)

## Registration Fee includes

- 1) Course Materials
- 2) Participation Certificate
- 3) Tea & Snacks

## Registration Fee

UG & PG Students Rs.100/-

## Importance of Course

Xilinx and Altera are the current FPGA market leaders and long-time industry rivals. Together, they control over 80 percent of the market, with Xilinx alone representing over 50 percent.

Xilinx also provides free Windows and Linux design software, while Altera provides free Windows tools; the Linux tools are only available via a rental scheme.

## Xilinx University Program



## Show Case:-

- Xilinx Spartan III VLSI Design Board
- Xilinx Spartan III EDK Development Board for Embedded applications
- Xilinx Spartan III Advanced Project Development Board
- Xilinx Virtex II Pro NetFPGA Board for networking applications
- Xilinx Virtex V Advanced Development Board for VLSI based applications
- Altera DE2 Advanced Development Board for VLSI applications

## Co-Ordination with



## Correspondence

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## One-Day Workshop on

## “CONCEPTS ON VLSI WITH XILINX PRODUCTS”

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(Saturday)

**General Chairman**  
**Dr.S.Ganapathy, Principal**

**Technical Chairman**  
**Dr.T.K.Mahendra Babu, Advisor**

**Convener**  
**Dr.A.V.Ramprasad, Professor**

**Coordinator**  
**Mr. V. Muthu Ganesh**



Organized by  
**Department of Electronics &  
Communication Engineering**  
(Accredited by NBA, New Delhi)  
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