

## About the Institute

K.L.N. College of Engineering has been the first self-financing co-educational Engineering College started in Madurai in 1994. The College has been affiliated to Anna University and approved by All India Council for Technical Education (AICTE). The College is located in the south eastern outskirts of Madurai and is 11Km away from Madurai city. The college runs 7 undergraduate engineering programs and 5 Master Programs including M.E. Communication Systems.

## Department of ECE

The Department of ECE was started in 1994. It has an intake of 120 students. The Department has adequate infrastructure with spacious classrooms, conference halls and well developed eight laboratories having the advanced designing tools like MATLAB, Orcad P-Spice, Xilinx, IE3D and modernized equipments like spectrum analyzer, microwave power meter, GPS kit. The core members of the faculty have rich academic experience and wide industrial and R&D exposure and are well suited for minimizing the gap between academy and industry. The Department initiates the U.G., P.G. & research projects in Embedded Systems through the Centre for Embedded Systems which is based on the collaboration of institution & Embedded industry. The Department library has over 1500 books and magazines and journals. AICTE, New Delhi has sanctioned fund as under MODROB scheme for modernizing Microwave Lab of this department.

## CoreEL Technologies

CoreEL is the Manufacturer's Representative of Xilinx in India, responsible for providing sales and technical support to customers in India. CoreEL University Program, which partners With Xilinx University Program and Higher Education Program of Mentor Graphics along with the University Programs of Many top VLSI companies in the World to bring you the World's best VLSI tools at affordable prices. CoreEL is the Authorized distributor & training partner of Mentor Graphics in India.

## Objectives of the Workshop

This workshop brings experienced FPGA designers to speed up the capabilities and characteristics of Xilinx processors. Time will be spent on learning the steps of the Xilinx embedded design flow from system design to bitstream generation, and how the various tools in the Xilinx Embedded Development Kit (EDK) encompass these steps. Attendees will get the chance to learn these steps through a series of hands-on lab exercises, as well as to perform on-chip hardware/software verification.

This course provides Professors with an introduction to FPGA-based DSP design flow using the Mathworks MATLAB®/Simulink® and Xilinx System Generator for DSP tools.

With



## Two-Day Workshop on “Embedded & DSP Design Flow Workshop in Xilinx Platform for Researchers”

29-07-2010 & 30-07-2010  
(Thursday & Friday)

**REGISTRATION FORM**

Name :

Qualification :

Designation :

Experience :

Department :

Organization :

Address for :

Communication

Phone No. :

Mobile No. :

E-mail id :

Accommodation required : Yes  No

D.D. No. : Dated:

Amount : Bank :

Date: Signature of the Applicant

### **Accommodation**

No TA / DA will be paid to the participants. On prior request, accommodation can be arranged in our college hostel with breakfast & dinner at a nominal fee.

### **Who can Register?**

The programme is open for all engineering faculty members & industrial persons. Interested Research Scholars & P.G. students are also allowed to register. Application can be downloaded from the website [www.klnce.edu](http://www.klnce.edu)

### **Registration Fee includes**

- 1) Course Kit
- 2) Course Materials
- 3) FDP Certificate
- 4) Working Lunch, Tea & Snacks

### **Registration Fee**

- 1) Delegates from Academic Institution and Industry Rs.750/-
- 2) Research / PG Scholars Rs.350/-

The registration fee should be paid through Demand Draft drawn in favour of "THE PRINCIPAL, K.L.N. COLLEGE OF ENGINEERING" payable at Madurai.

### **Local Transportation**

Our college buses are plying on daily over 15 routes from various parts of city. Participants can use these buses to reach K.L.N.C.E. For further information please contact coordinators.

### **Important Dates**

Registration form along with D.D should reach us on or before 26<sup>th</sup> July 2010. Limited spot registration only be allowed.

## **Xilinx University Program**

### **Benefits to Acquire:**

- Rapid Architect on an embedded system for targeting to interface with the hardware platform
- Extend the hardware system with Xilinx based peripherals
- Create a custom peripheral and add it to the system
- Write software code to access peripherals
- Write interrupt handlers to service interrupts
- Perform an on-chip hardware/software verification
- Understand why FPGAs lend to high-performance DSP design
- Understand the basics of Simulink
- Identify useful blocks in the System Generator blockset
- Model and Simulate a System Generator design in Simulink
- Understand the hardware impact of various parameter settings
- Understand the mechanisms for control
- Understand the underlying clocking for multirate systems

### **Correspondence**

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K.L.N. College of Engineering  
Pottapalayam – 630 611.  
Madurai, Tamilnadu.

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**Chairman  
Dr.S.Ganapathy, Principal**

**Technical Chairman  
Dr.T.K.Mahendra Babu, Advisor**

**Convener & H.O.D - ECE  
Dr.A.V.Ramprasad, Professor**

**Coordinators  
Mr.D.Pravin Kumar  
Mr.R.Mohan Kumar**



**Organized by  
Department of Electronics & Communication  
Engineering**

(Accredited by NBA, New Delhi)  
**K.L.N.COLLEGE OF ENGINEERING**

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